



Jair GONZALEZ, Technical Director

Experience

Jair joined Ansys Esterel Technologies in 2013 as field application engineer, promoted as technical director in 2014.

From 2004 to 2009, Jair was Hardware Design Lead at Tecnológico de Monterrey, Mexico. From 2003 to 2004, Jair was Design and Development engineer of FPGA-based systems at CINVESTAV in Guadalajara, Mexico. From 1998 to 2000, Jair was Firmware Design Engineer at Hewlett-Packard in Guadalajara, Mexico.

Education

MINES ParisTech

- **2009:** Doctor of Philosophy (Ph.D.), Communications and Electronics, MINES ParisTech
- **2003:** MSc Computer Science, Danmarks Tekniske Universitet
- **1997:** Bachelor's Degree, Electronic Systems