



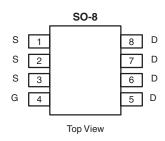
# N-Channel 60-V (D-S) MOSFET

| PRODUCT SUMMARY     |                                 |                    |  |
|---------------------|---------------------------------|--------------------|--|
| V <sub>DS</sub> (V) | $R_{DS(on)}(\Omega)$            | I <sub>D</sub> (A) |  |
| 60                  | 0.024 at V <sub>GS</sub> = 10 V | 7.5                |  |
|                     | 0.03 at V <sub>GS</sub> = 6.0 V | 6.5                |  |

#### **FEATURES**

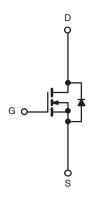
- Halogen-free According to IEC 61249-2-21 Available
- TrenchFET<sup>®</sup> Power MOSFET





Ordering Information: Si4450DY-T1-E3 (Lead (Pb)-free)

Si4450DY-T1-GE3 (Lead (Pb)-free and Halogen-free)



N-Channel MOSFET

| <b>ABSOLUTE MAXIMUM RATINGS</b> T <sub>A</sub> = 25 °C, unless otherwise noted |                        |                                   |             |      |  |
|--|------------------------|-----------------------------------|-------------|------|--|
| Parameter  |                        | Symbol                            | Limit       | Unit |  |
| Drain-Source Voltage   |                        | V <sub>DS</sub>                   | 60          | V    |  |
| Gate-Source Voltage  |                        | V <sub>GS</sub>                   | ± 20        | V    |  |
| Continuous Dunis Comment /T 150 90\8   | T <sub>A</sub> = 25 °C | I <sub>D</sub>                    | 7.5         |      |  |
| Continuous Drain Current (T <sub>J</sub> = 150 °C) <sup>a</sup>                | T <sub>A</sub> = 70 °C |                                   | 5.5         |      |  |
| Pulsed Drain Current   |                        | I <sub>DM</sub>                   | 50          | А    |  |
| Continuous Source Current (Diode Conduction) <sup>a</sup>                      |                        | I <sub>S</sub>                    | 2.1         |      |  |
|  | T <sub>A</sub> = 25 °C | P <sub>D</sub>                    | 2.5         | W    |  |
| Maximum Power Dissipation <sup>a</sup>   | T <sub>A</sub> = 70 °C |                                   | 1.6         | ] ** |  |
| Operating Junction and Storage Temperature Range                               |                        | T <sub>J</sub> , T <sub>stg</sub> | - 55 to 150 | °C   |  |

| THERMAL RESISTANCE RATINGS               |                   |       |      |  |
|--|-------------------|-------|------|--|
| Parameter                                | Symbol            | Limit | Unit |  |
| Maximum Junction-to-Ambient <sup>a</sup> | R <sub>thJA</sub> | 50    | °C/W |  |

#### Notes:

a. Surface Mounted on FR4 board,  $t \le 10 \text{ s.}$ 

For SPICE model information via the Worldwide Web: http://www.vishay.com/www/product/spice.htm

# Vishay Siliconix



| Parameter                                     | Symbol              | Test Conditions   | Min. | Typ. <sup>a</sup> | Max.  | Unit |  |
|---|---------------------|---|------|-------------------|-------|------|--|
| Static  |                     |   | •    | •                 |       |      |  |
| Gate Threshold Voltage                        | V <sub>GS(th)</sub> | $V_{DS} = V_{GS}$ , $I_D = 250 \mu A$                                 | 2    |                   |       | V    |  |
| Gate-Body Leakage                             | I <sub>GSS</sub>    | $V_{DS} = 0 V, V_{GS} = \pm 20 V$                                     |      |                   | ± 100 | nA   |  |
| Zero Gate Voltage Drain Current               | I <sub>DSS</sub>    | $V_{DS} = 60 \text{ V}, V_{GS} = 0 \text{ V}$                         |      |                   | 1     | μΑ   |  |
|   |                     | V <sub>DS</sub> = 60 V, V <sub>GS</sub> = 0 V, T <sub>J</sub> = 55 °C |      |                   | 20    |      |  |
| On-State Drain Current <sup>b</sup>           | I <sub>D(on)</sub>  | V <sub>DS</sub> = 5 V, V <sub>GS</sub> = 10 V                         | 20   |                   |       | Α    |  |
| Drain-Source On-State Resistance <sup>b</sup> |                     | $V_{GS} = 10 \text{ V}, I_D = 7.5 \text{ A}$                          |      | 0.020             | 0.024 | Ω    |  |
|   | R <sub>DS(on)</sub> | $V_{GS} = 6.0 \text{ V}, I_D = 6.5 \text{ A}$                         |      | 0.025             | 0.03  |      |  |
| Forward Transconductance <sup>b</sup>         | 9 <sub>fs</sub>     | V <sub>DS</sub> = 15 V, I <sub>D</sub> = 7.5 A                        |      | 18.5              |       | S    |  |
| Diode Forward Voltage <sup>b</sup>            | $V_{SD}$            | I <sub>S</sub> = 2.1 A, V <sub>GS</sub> = 0 V                         |      | 0.75              | 1.2   | V    |  |
| Dynamic                                       | l I                 |   |      | 1                 |       |      |  |
| Total Gate Charge                             | $Q_g$               |   |      | 31                | 50    | nC   |  |
| Gate-Source Charge                            | $Q_{gs}$            | $V_{DS} = 30 \text{ V}, V_{GS} = 10 \text{ V}, I_{D} = 7.5 \text{ A}$ |      | 7.7               |       |      |  |
| Gate-Drain Charge                             | $Q_{gd}$            |   |      | 8.3               |       |      |  |
| Gate Resistance                               | $R_g$               |   | 1    |                   | 5.8   | Ω    |  |
| Turn-On Delay Time                            | t <sub>d(on)</sub>  |   |      | 16                | 30    |      |  |
| Rise Time                                     | t <sub>r</sub>      | $V_{DD}$ = 30 V, $R_L$ = 30 $\Omega$                                  |      | 11                | 20    | ns   |  |
| Turn-Off Delay Time                           | t <sub>d(off)</sub> | $I_D \cong 1 \text{ A}, V_{GEN} = 10 \text{ V}, R_g = 6 \Omega$       |      | 41                | 80    |      |  |
| Fall Time                                     | t <sub>f</sub>      |   |      | 21                | 40    |      |  |
| Source-Drain Reverse Recovery Time            | t <sub>rr</sub>     | I <sub>F</sub> = 2.1 A, dI/dt = 100 A/μs                              |      | 46                | 80    |      |  |

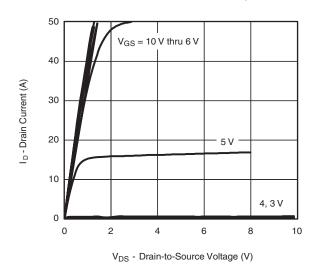
#### Notes:

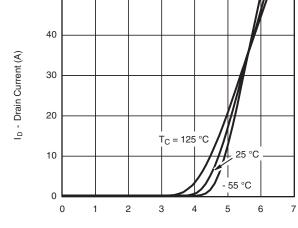
- a. For design aid only; not subject to production testing.
- b. Pulse test; pulse width  $\leq$  300  $\mu$ s, duty cycle  $\leq$  2 %.

Stresses beyond those listed under "Absolute Maximum Ratings" may cause permanent damage to the device. These are stress ratings only, and functional operation of the device at these or any other conditions beyond those indicated in the operational sections of the specifications is not implied. Exposure to absolute maximum rating conditions for extended periods may affect device reliability.

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#### TYPICAL CHARACTERISTICS 25 °C, unless otherwise noted





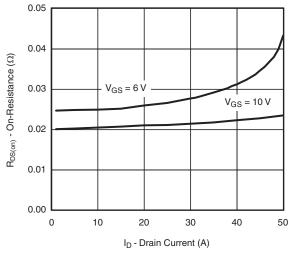
V<sub>GS</sub> - Gate-to-Source Voltage (V)

**Transfer Characteristics** 

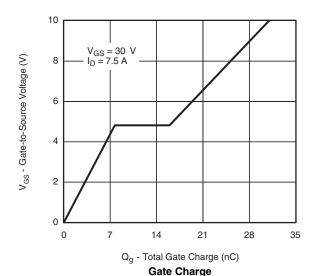


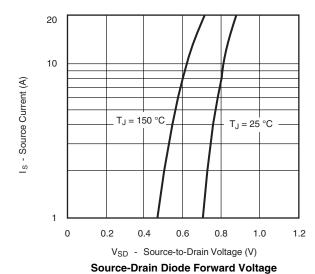


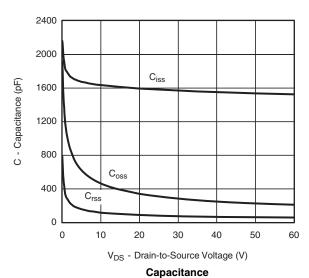
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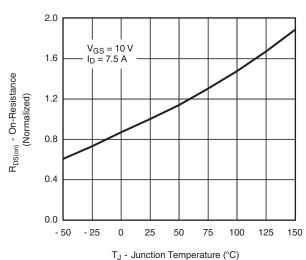


#### On-Resistance vs. Drain Current

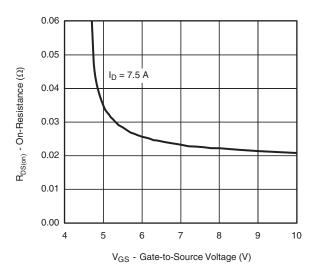








On-Resistance vs. Junction Temperature

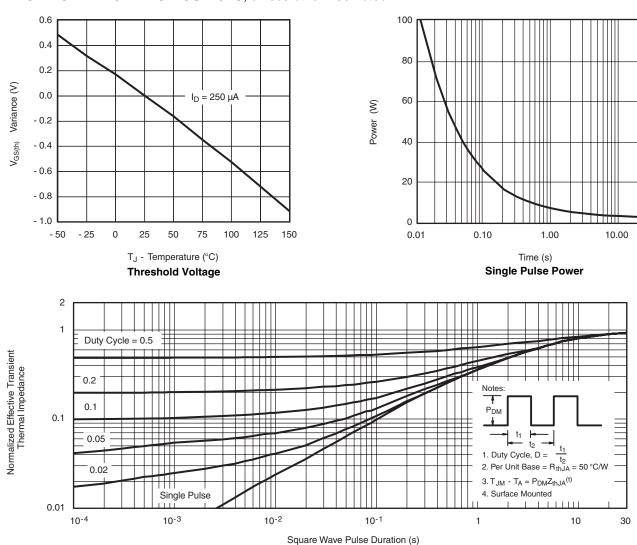


On-Resistance vs. Gate-to-Source Voltage

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## TYPICAL CHARACTERISTICS 25 °C, unless otherwise noted



Normalized Thermal Transient Impedance, Junction-to-Ambient

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