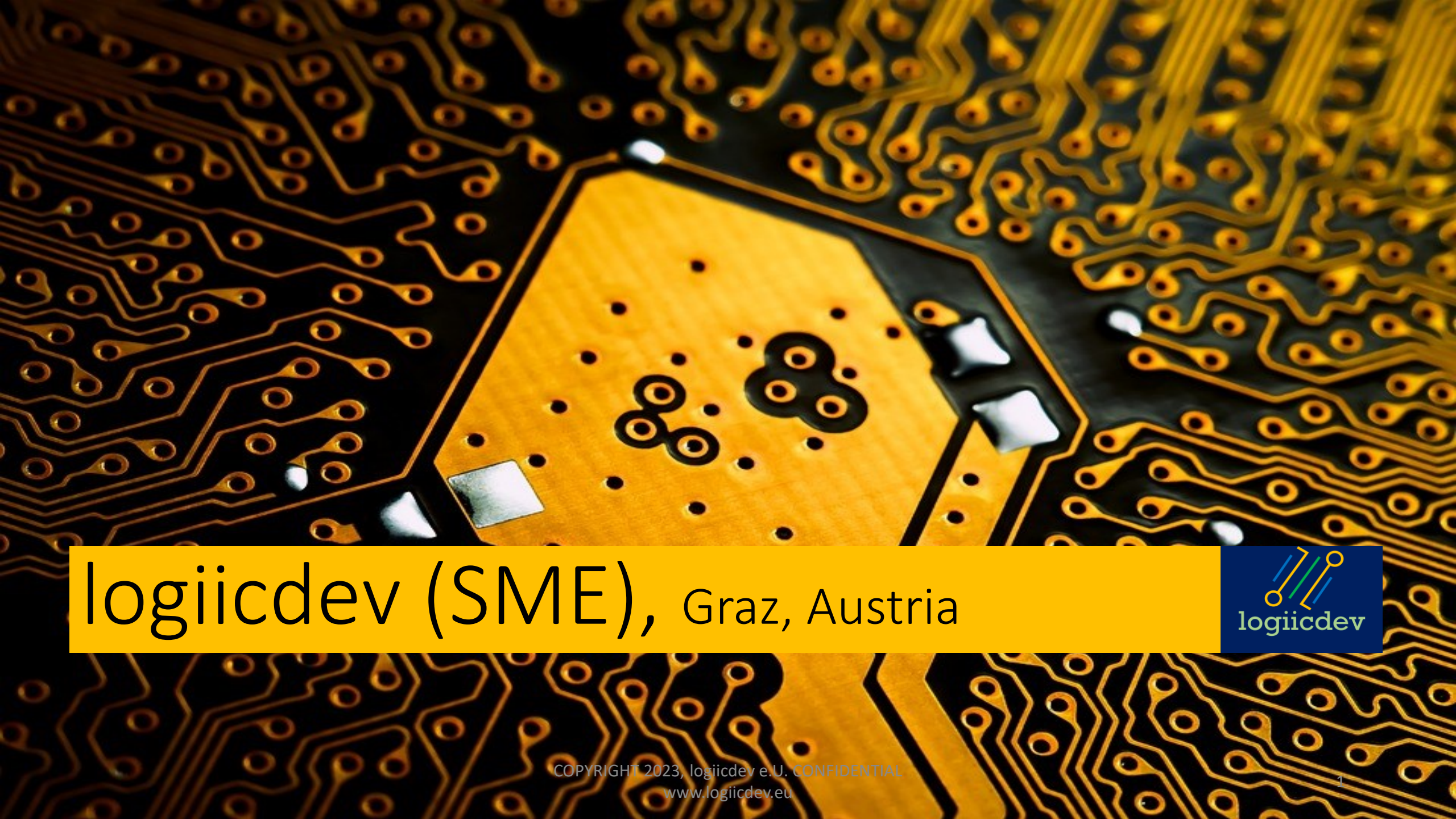




logiicdev (SME), Graz, Austria

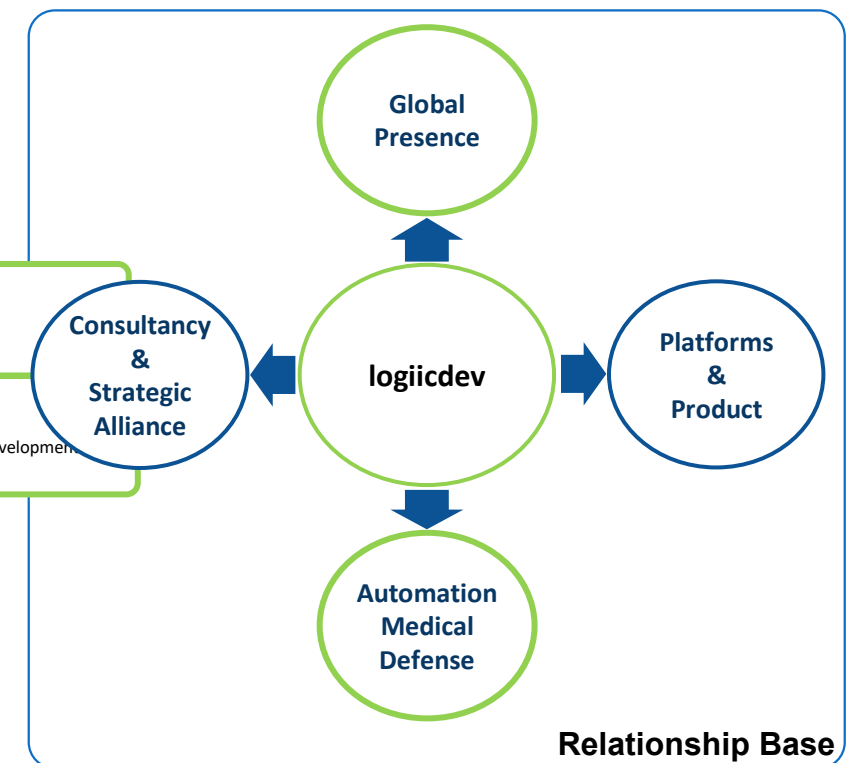


Skills

8 / 16 / 32 Bit Processors	PCB design	FPGA / Semiconductors
• ATMEL – AVR, TI – MSP430, Sitara • NXP - Kinetic • Microchip – PIC • ARM-7, ARM-9, Cortex M4 • ST –STM H7 series	• 12+ layer PCB Design • High Speed, SerDes, Ethernet, • HDI, Control Impedance PCBs • SI / PI, DFM analysis	• Xilinx – Zynq, Artix, Virtex, Kintex • Altera – Cyclone, MAX series • HW – SW Co-Verification, Emulation
Hands-on		
• Memories - DDR-IV, DDR-III, SODIMM, Nor Flash, NAND Flash, SRAM, SD Cards • Power Electronics (PE) – Controller, Driver, Mosfet compensation network and spice simulation • FPGA –MIPI camera, RFFE, display, C/D Phy, RF Communication and Sensor Telemetry • Others – BMS, Super capacitor, Charging topologies, controllers, RLC measruemt, ADCs-DACs, etc.		
Tools		
Tools, and IDE	PCB Design and SI	FPGA Development
• Code Composer Studio, ATMEL Studio • Microchip – MPLAB • QT, VS, C++	• Cadence Allegro, Mentor Graphics Pads, Altium • Hyperlynx, Siwave, Virtuso for PI & SI	• Xilinx – Vivado • Altera – Quartus II

- System Architecture
- Ownership and Execution
- Cost Engineering

- Scalable Solution
- Technical Support and Development



Company Intro

Austrian-based high-tech SME company is working on AI-based test equipment and FPGA IP. we have a strong team of FPGA, SW, HW, FW, and python-based algorithm developments.

A team made up of Theo H., Ian M., and Deepak V. K. founded logiicdev in January 2020 in Graz, Austria. Combined, the company brings over 100 years of experience of electronics domain. Our primary goal is to establish ourselves as a leader in sustainable and AI-based eco-friendly electronics design. With a strong team of industry experts, logiicdev is well-equipped in simulation (logic, die, package, PCB), design (HDL & PCB), commercialization, and sales.

The founder of logiicdev, MSc Deepak V. Katkoria, brings a wealth of experience, having worked with over 22 industry leaders, contributing to various stages of design and sales processes. Currently, logiicdev has a highly experienced team in place to drive its success. This includes Mr. Theo Hatzis, the Chief Technology Officer, who possesses over 40 years of Power electronics and sensor experience. Mr. Ian Macnamara, also with over 40 years of experience in RFID, NFC, and management. logiicdev continually seeks out experts in various fields to solves the industry challenges. By expanding our team, we aim to accelerate our researched based business growth and build strong customer relationships. logiicdev's emphasis is to lead engineers and designers to develop cutting-edge electronics based solution. A prioritised environmental sustainability, efficiency, reliability, digitalised, performance optimisation, recyclability, and integration with new technologies is key to going forward.

Company has provided sucessfully the FPGA, AI testing, IOT, & BLE solution for laboratory monitoring, and car parking.

Infrastructure

Description of any significant infrastructure and/or any major items of technical equipment, relevant to the proposed work.

Name of infrastructure of equipment	Short description (Max 300 characters)
Hardware Test and Development Laboratory	Hardware Test and Development Laboratory. This laboratory is equipped with hardware design and circuit tools, oscilloscopes, and signal analyzers, Power supply, signal generator, 50w discharging load, prototyping utilities, and other related hardware which can be used for developing and
EDA simulation machine	128GB RAM simulation multicore server specially for AI model training, FPGA and Simulation purposes. Inhouse Cloud server 4TB. Computer workstations will constitute a computational power used to carry out numerical simulations in order to design and optimize (WP2,5&6).
FPGA tool	IDE tool (Intel Quartus, Vivado, Libero SoC Design Suite), EDA software (Altium), Spice+Psice simulation tool (spice, multi si), scientif calculations (Octave -Matlab alternative), GUI development (Python and QT), CAD software (SolidWorks): will be used for the preparation of the FPGA, P
Rework Station	Workstations, Rework and soldering capabilities, connector assembly, wiring and component storage facility (WP5 &7).

Passion is success



Founder,
MSc Deepak v Katkoria

CTO,
MSc Thoe Hatzis

Investor,
Mr. Ian Macnamara



Presented the Battery challenges on AIMday Materials 2023 /Sweden to academic researchers.

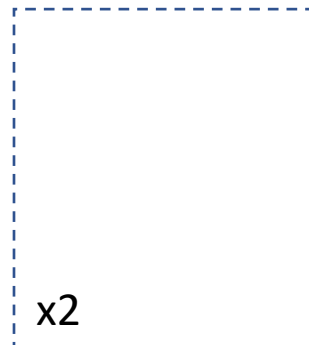
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Science Park
The High Tech Incubator
Graz



**business
incubation
centre**
Austria

MSc students



x2

Full time

**Firmware Developer x1
Power Electronics Engineer
x2
PCB and test engineer x1
FPGA engineer x1**

x5

**Manager,
Ms Darshana D K**



Customers



Thank You



Industrial AI based Controller (IOT client)

Contact Us

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