

Precision in Every Layout. Innovation in Every Design.

IC Layout Design & Engineering Services



Analog



Mixed-Signal



RF



High-Voltage



Semi-Custom



PCB

Epical Layouts Private Limited

Chennai, India | Serving Semiconductor Companies Worldwide

IDEAS TO SILICON TO A BRIGHTER TOMORROW

Your IC Layout Engineering Partner

Epical Layouts is a specialized semiconductor design services company providing custom IC layout and physical design expertise to semiconductor companies worldwide.

We work as an extension of your engineering teams — from individual blocks to complete chip-level integration and tape-out support.

WHAT DEFINES US



Specialized Expertise

Layout-focused engineering team with deep technical knowledge.



Flexible Engagement

Project-based or dedicated engineering resources to suit your needs.



Scalable Capacity

Additional layout bandwidth when your project demands increase.

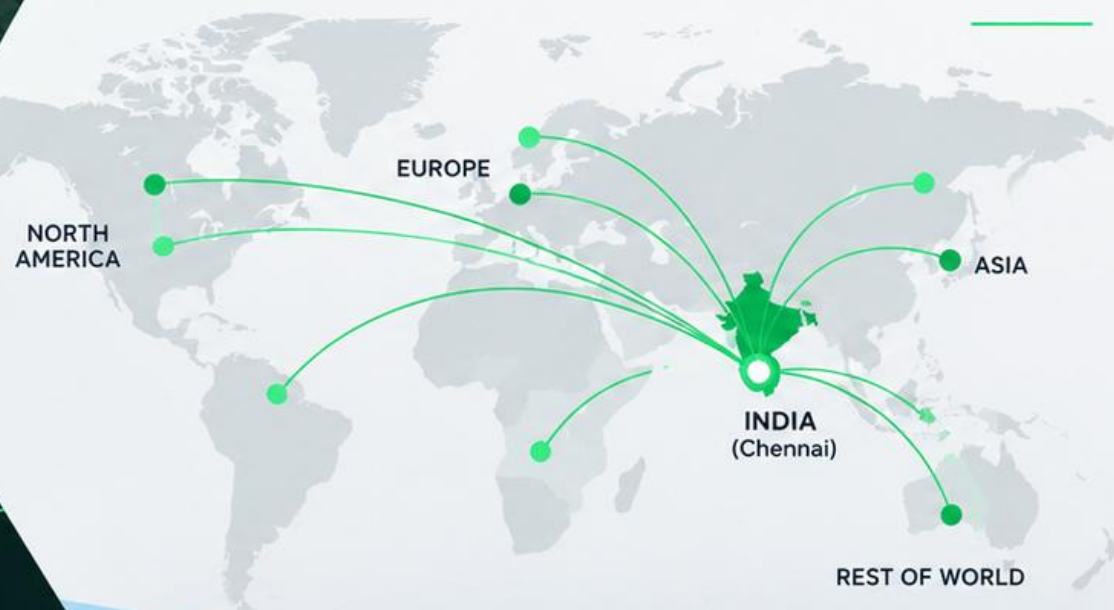


Global Collaboration

Experience supporting international semiconductor teams.

EPICAL
L A Y O U T S

GLOBAL COLLABORATION.
REAL IMPACT.



A LONG-TERM
PARTNER FOR
A BRIGHTER TOMORROW.

From Block to Tape-Out

Comprehensive IC layout engineering services to turn your designs into silicon.

IDEAS
LAYOUTS
SILICON
REAL IMPACT

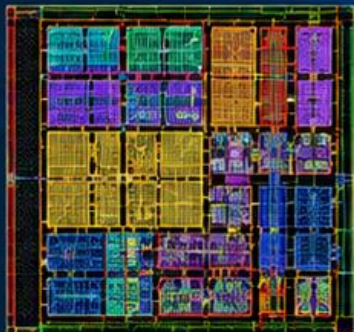
EPICAL
L A Y O U T S

PRECISION IN EVERY LAYOUT.
INNOVATION IN EVERY DESIGN.

01

Full-Custom Layout

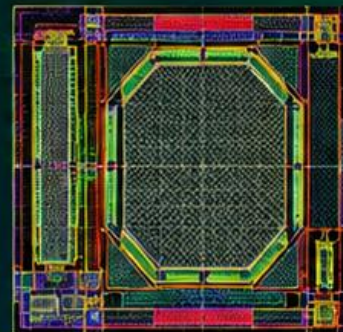
- Analog
- Mixed-Signal
- RF
- Precision & Matching



02

High-Voltage Layout

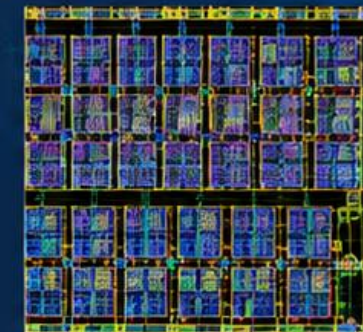
- HV CMOS
- BCD
- Power IC
- Isolation & Reliability



03

Semi-Custom Layout

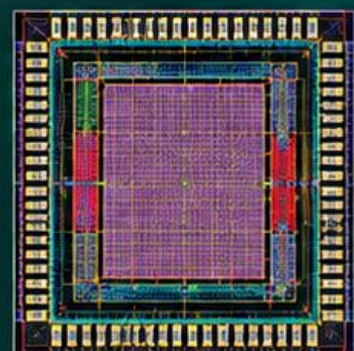
- Digital Blocks
- Standard Cells
- Macro Integration
- Customized Solutions



04

Top-Level Integration

- Floorplanning
- IO / Pad Ring
- Power Planning
- Chip Assembly



05

Physical Verification

- DRC
- LVS
- PEX
- Density & Fill
- Reliability Checks



06

Tape-Out Support

- Final Verification
- GDS Delivery
- Sign-Off Support
- Foundry Submission Assistance



Block Layout

Design & Implementation



Integration

Top-Level Assembly



Verification

DRC / LVS / PEX



GDS

Tape-Out Ready



To Silicon

Your Ideas. Our Layouts.

Across Technologies. Across Applications.

Layout expertise across a wide range of semiconductor technologies.

TRUSTED.
FLEXIBLE.
FUTURE READY.

PRECISION IN EVERY LAYOUT.
INNOVATION IN EVERY DESIGN.

Bulk CMOS

Analog & Mixed-Signal
Applications



Mature technology
Cost-effective
Wide application base

FD-SOI

Low-Power & RF
Applications



Low power
High performance
RF & Analog suited

HV CMOS

High-Voltage
Applications



High-voltage devices
Robust design
Industrial & automotive

HV-BCD

Power Management
Solutions



High-voltage + Bipolar
Power ICs
Wide application range

BCD-on-SOI

High-Voltage &
Isolation Applications



High-voltage performance
Improved isolation
Analog, power & mixed-signal

GaN-on-Si

HEMT / Power / RF
Applications



Wide bandgap
High efficiency
RF & power applications

GaN-on-SOI

Advanced GaN
Integration



High isolation
RF & power
Next-generation integration

FinFET

Advanced-Node
IC Design



High performance
Low power
Scaling for complex SoCs

GAAFET

Next-Generation
Technology



3D device architecture
Higher drive current
Future-ready designs

From mature high-voltage technologies to **advanced nodes**.

ENABLING A SMARTER,
MORE CONNECTED TOMORROW.

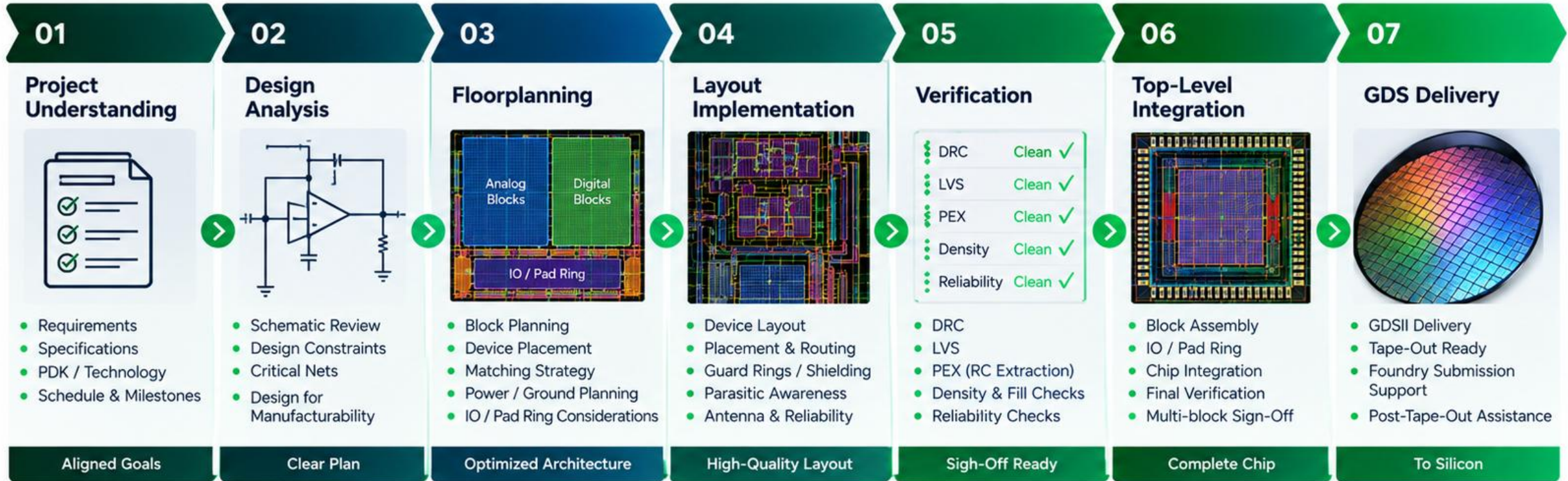
From Your Design to Tape-Out

A structured, transparent and quality-driven approach to deliver reliable layouts.

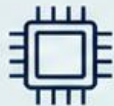
COLLABORATE
DESIGN
VERIFY
DELIVER

EPICAL
L A Y O U T S

PRECISION IN EVERY LAYOUT.
INNOVATION IN EVERY DESIGN.



**Beyond
the Chip**



Package Planning

- Package Selection
- Pin Mapping / Package Drawing



PCB / Evaluation Board

- PCB Layout (Product / Evaluation Board)
- Design for Testing & Validation

SUPPORTING
YOUR INNOVATION
FROM SILICON TO SYSTEM.

Engineering Support That Scales With Your Roadmap.

Flexible engagement models to match your project needs — from a single block to a dedicated team.

EPICAL
L A Y O U T S

PARTNER
SCALE
DELIVER
GROW

PRECISION IN EVERY LAYOUT.
INNOVATION IN EVERY DESIGN.

01

Dedicated Engineer

1+ Engineer



- Works directly alongside your internal design team
- Seamless collaboration
- Quick ramp-up on your flow and tools

Best for: Long-term development
Continuous layout requirements

02

Dedicated Team

2 – 10+ Engineers



- Scalable Epical team aligned to your project roadmap
- Managed coordination and reporting
- Flexible team size and skill mix

Best for: Multiple blocks
Parallel developments
Long-term programs

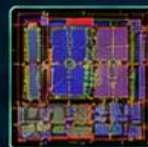
03

Project Based

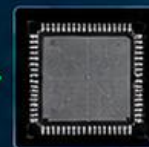
Block → GDS



Schematic



Layout



GDS

- Epical takes responsibility for defined layout deliverables
- Clear scope, milestones and quality
- End-to-end support till tape-out

Best for: Specific IP / block
Fixed project scope
Tape-out requirements

04

Peak Capacity Support

Scale When Needed



YOUR
PROJECTS.
OUR
ADDITIONAL
BANDWIDTH.

- Additional engineering bandwidth during critical project phases
- Experienced engineers with quick onboarding
- Helps you meet tight schedules

Best for: Tape-out peaks
Schedule recovery
Temporary resource gaps



One block or a complete layout team — **scale when your roadmap demands it.**

YOUR LAYOUT
PARTNER FOR A STRONGER
SEMICONDUCTOR TOMORROW.

From Silicon to System.

End-to-end support from IC layout to package planning and PCB realization.

EPICAL
L A Y O U T S

DESIGN
INTEGRATE
REALIZE
TOGETHER

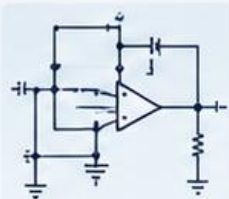
PRECISION IN EVERY LAYOUT.
INNOVATION IN EVERY DESIGN.



IC LAYOUT

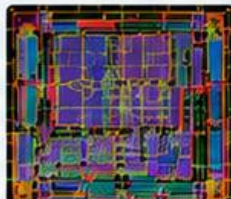
CUSTOM LAYOUT SOLUTIONS
FOR HIGH-PERFORMANCE ICs

Schematic



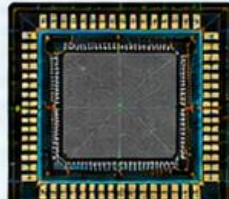
- Design Review
- Constraint Analysis
- Critical Nets

Custom IC Layout



- Device Matching
- Low Parasitics
- DFM Compliance

GDS / Tape-Out



- DRC / LVS / PEX
- Reliability Checks
- Tape-Out Ready

Package Planning



- Package Selection
- Pin Mapping
- Package Drawing

IC DESIGN → LAYOUT → VERIFICATION → GDS → PACKAGE



PCB ENGINEERING

FROM CHIP TO PRODUCT
WITH RELIABLE PCB DESIGNS

Schematic / Requirements



- Circuit Integration
- Component Selection
- Design Guidelines

Product Board



- Multi-layer PCB
- High-Speed Routing
- Signal & Power Integrity

Evaluation Board



- Prototype & Validation
- Design Optimization
- Test Readiness

Manufacturing Outputs



- Gerber Files
- BOM & Pick-and-Place
- Manufacturing Support

SCHEMATIC → PCB LAYOUT → PROTOTYPE → MANUFACTURING

APPLICATIONS →



Automotive



Industrial



Healthcare



Aerospace & Defense



Data & Communications



Consumer Electronics

SUPPORTING
A SMARTER,
MORE CONNECTED
TOMORROW.

Epical Layouts Private Limited

Chennai, India | Serving Semiconductor Companies Worldwide

PEOPLE | PROCESS | POSSIBILITIES

Your Next Chip Starts Here.

Whether you need a single critical block, additional tape-out support or a dedicated layout team, Epical Layouts can become an extension of your engineering organization.

Let's Discuss



For new business / collaboration
prashanth@epicallayouts.com



www.epicallayouts.com



Chennai, India



SCAN TO VISIT
OUR WEBSITE

EPICAL
L A Y O U T S

PRECISION IN EVERY LAYOUT.
INNOVATION IN EVERY DESIGN.



Analog



Mixed-Signal



RF



High-Voltage



Semi-Custom



PCB



SERVING
SEMICONDUCTOR COMPANIES
WORLDWIDE



PEOPLE

Skilled. Committed. Collaborative.



PROCESS

Structured. Reliable. Transparent.



POSSIBILITIES

From today's needs to tomorrow's innovations.

PRECISION IN EVERY LAYOUT.
INNOVATION IN EVERY DESIGN.

IDEAS
LAYOUTS
SILICON
REAL IMPACT