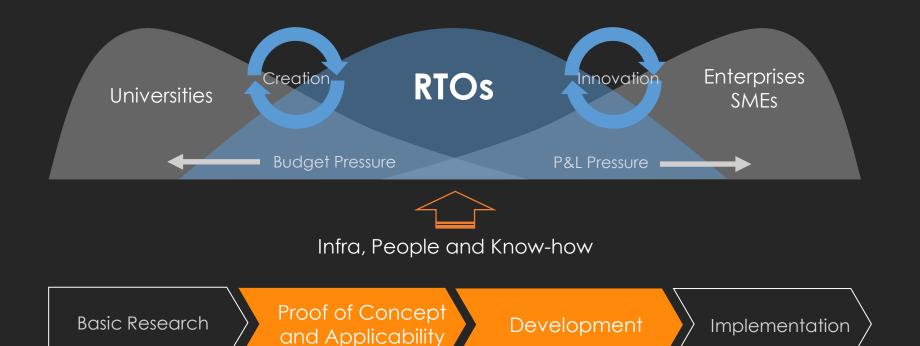




Productivity & Competitiveness



RTOs speeding up digital transformation



Global level











Google

Partner









aws

PARTNER



















Non refundable







Fiscal incentives









PPIs



IA / PNM



RNP / Saúde Digital



Mandatory investments









Our competences



Ready for 4.0 age!



26 years providing R&D services



Our numbers















Microelectronics

Design and Packaging



Integrated circuits projects end-to-end domain

• Front End: Digital / Verification

Analogical / Mixed Signal / RF / Power

DFT

• Back End: Physical Layout

Sign off verification

MEMS

Packaging: Layout / Routing

Signal Integrity / Reliability Analysis

Prototyping / Characterization / Qualification (JEDEC)

Technologies / Foundries

180nm | 65nm | 40nm | 28nm | 22nm | 12nm | 6nm





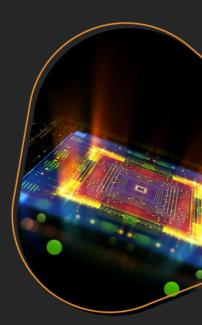




7International
Custormers

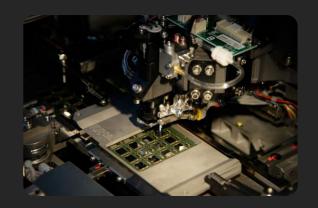
17 Tapeouts





















Hardware Development



20 years of experience in HW development 300+ projects / 100+ customers

End-to-end skills and capabilities

Multidisciplinary teams with complementary skills:

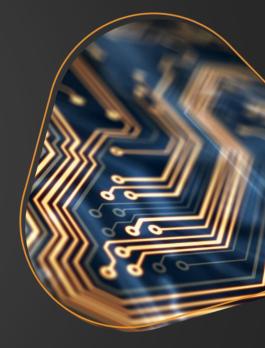
- System Engineering
- Analog and Digital Electronics
- Power Flectronics
- RF / EMC
- Embedded Software

Robust laboratorial facilities

 Telecomunications, EMC, Power Electronics, Mechanical and non-destructive analysis

Labs for development and certification

90



Development Tools:





















HW development



HW Design meeting Partner Needs

Model-Based System Engineering

Mathematical modelling and linearization

Precise simulation with Automatic code generation

Firmware validation with hardware in the loop

Hardware validation by lab prototyping

Product validation by standard testing



PCB Design

High Density

Small Pitch

Multiple Layers

Miniaturized Projects

EMC Robust Design

RF Customization



Fit to International Standards

Complying with ISO, IEC, FCC, CE Mark and UL

VW80000 and TL81000 Evaluation

Design for X Approach:
Manufacturing, Testing,
Reliability etc.

18

of layers

0.1mm
Blind Vias

0.051mm

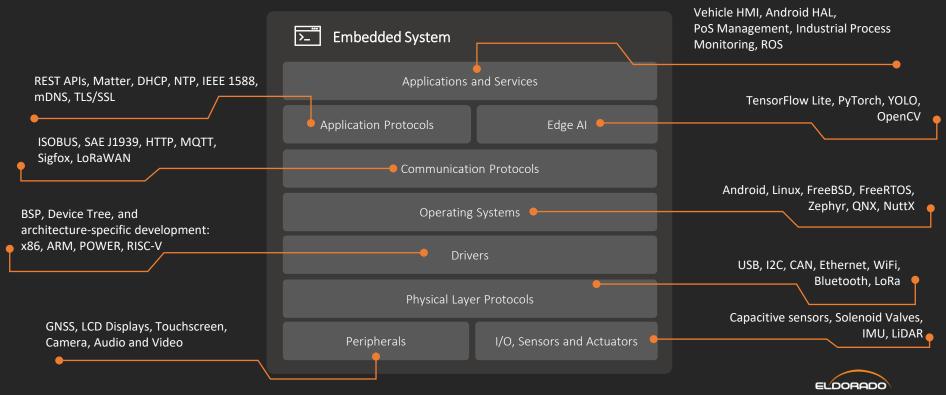
Trace Width

01005 Component size





Embedded SW





Embedded SW - Android

Development process and scope

Requirements	Kernel Drivers Bootloader	Framework HAL Performance	Bugfix and Certificat	Post Launch Support
		Security		

Ownership of Embedded SW enables:

- Innovation
- Fast interaction with Integration Team
- Factory and supply chain changes support
- Customer and field issues support
- Early Access (A+1)

We develop, deliver and support Android system software upgrades for a major player on the global smartphone industry

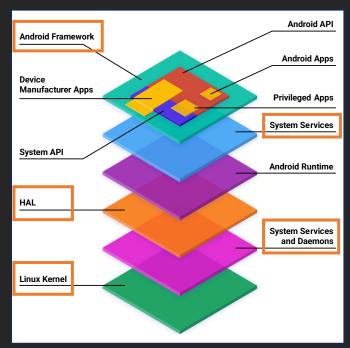


Image from AOSP Documentation



Embedded SW



Android **OS** Upgrades

Since Android 6

Ready to Android 15 (early access)



Working closely with

Qualcomm MediaTek Infineon NXP Toradex **NVIDIA**



All Embedded SW layers

> Kernel Sensors Connectivity Performance Cyber-security

> > Know-how



Architecture Experience

> x86 ARM Power RISC-V



Contributions



Operating Systems



















Process and Tools

CI/CD GIT Gerrit **Jenkins** Artifactory

10+ Years

Of experience with **Embedded Software** Development

150+

FTE ELDORADO





#0\$





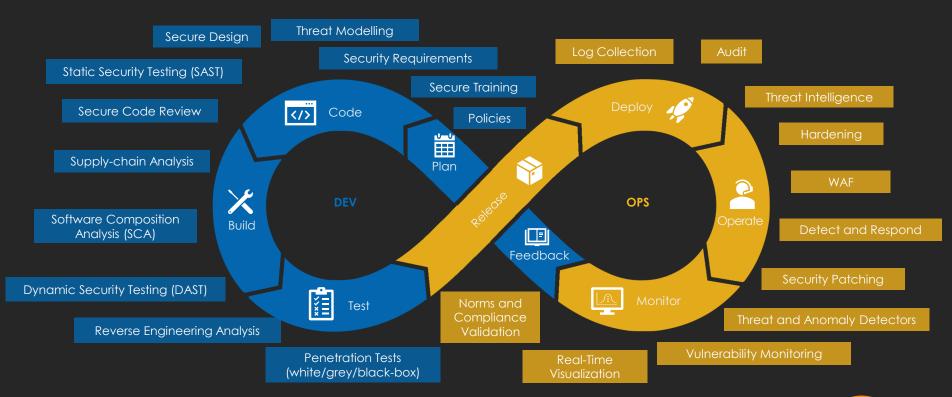








Cibersecurity



SW application



Software Development

Mobile Apps (Android, Automotive and iOS) and Web Apps



Quality Assurance

Software Verification Software Validation



User Experience

Research Flows Final User Interface University Partnership



Work Process

CI/CD Devops Security by Design



Architecture Design

High Transition
Volume
Flexibility
Security



Machine Learning Models

Data Image Sensors



Cloud Infrastructure

Serverless Multi-cloud



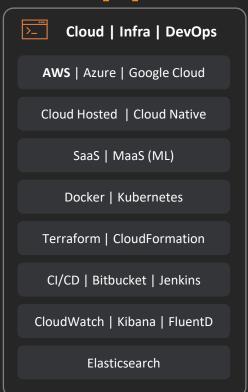
Years of experience with SW application development

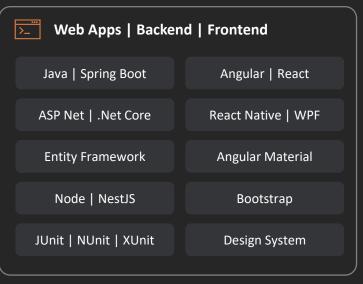
500+

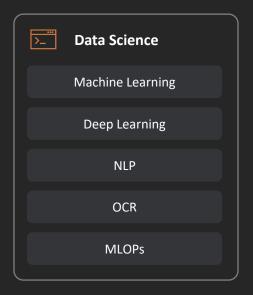
FTE ELDORADO

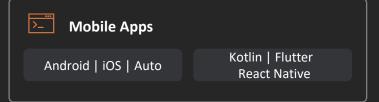


SW application











Camera & Image





Global Camera Development end to end solution

Lab

Image Validation & QA

AI & ML

Algorithm & SW Dev Integ.

R&D

State of Art

SW Dev

Camera Embedded, Bring up/ **Quality Management**











global labs

world wide reference with DXO and VCX partnership

images on database for ML study and Al development

universities

partnership to develop State of Art algorithms and overall imaging technology

83 products

with different Chipset and SW solutions deployed in the Market



Visual Computing



Image Processing

Camera ISP Pipeline Al Models & Datasets for low level image tasks



Computer Vision

Al Models & Datasets for classification, detection, tracking, ...
Use of synthetic data



Digital Realities & Spaces

VR/AR/XR Metaverse Digital Twins



Surveillance & Safety

People and vehicle tracking Electronic fencing Process visual monitoring Human behavior analysis



Automation

OCR (printed and displayed) Custom barcode readers Non-rigid product counting Food goods classification



Quality Inspection

Electronics boards defects Assembled products Screws, cables, connectors 3D parts



Extended Reality

Immersive training
Virtual product experience
AR product usage guidance

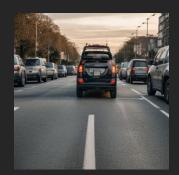


Generative Al for ADAS

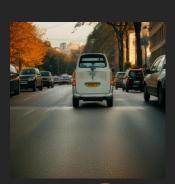
- Generative AI applied to cover corner cases in ADAS training and validation in video injection
- Real image customization (eg: changing weather)
- Image creation from simple 3d scenes (eg: dangerous braking)
- Image to video transformation
- ML skills as GAN, Diffusion and Optical Flow



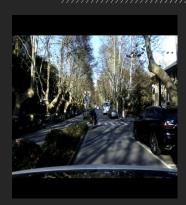


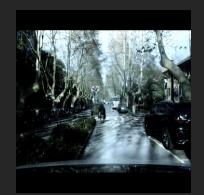












Real time video analytics

Challenges:

- Embedded AI used for automotive safety area. The idea is to monitor the attenction and actions pof the truck driver, applied AI in the cameras inside truck cockpit.
- Al Model Optimization in order this models could leverage HW acceleration, reaching 10x more performance.

Tech Detail:

- skills: ML, deep learning, NN, visual computing and embedded systems
- Plataform: NVIDIA Jetson Orin Nano (4 e 8 GB), NVIDIA TensorRT
- Code originally required 3 CPUs Intel, after Eldorado otimization work now runs in 1 Jetson (1 CPU + 1 GPU)
- Improve in performance of 10x
- VC Features: Facial Recognition, Sleepiness detection, forbidden action detection(smoke), Environment monitoring (speed, object and dangerous situations detection and warning)





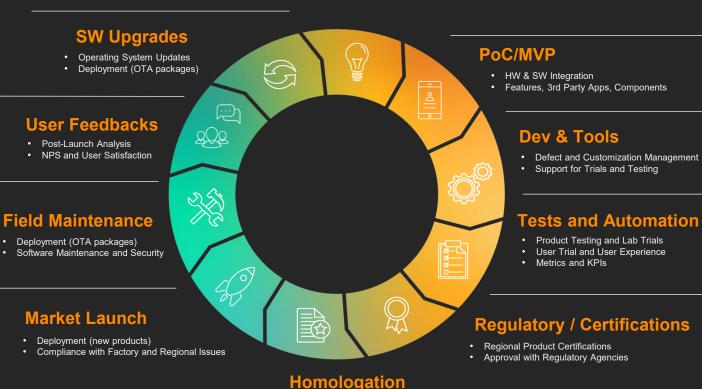




Competencies in



Product Integration and Validation



300+

FTE ELDORADO

10+

Years of Expertise in SW integration

~50M

Users / Month

140M+

Active Users

70TB

Data Pipeline



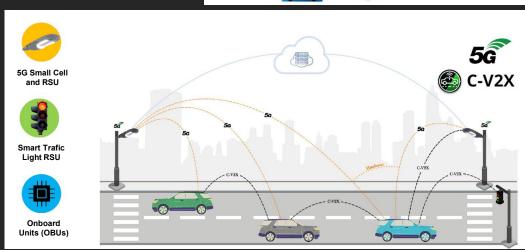


V2X - Connected Route

- Vehicle Connectivity Environment for Advanced Mobility Research:
 - Testbed environment
 - Vehicle Connectivity Testing and Prototyping
 - 5G & C-V2X connectivity
 - Eldorado + Universities + Automotive Companies
- Connectivity Prototyping Lab
 - Internal environment for 5G and V2X R&D
 - Experiment cases in Lab before deployment in production
 - Development of applications and applied use cases











Hil Test Automation

ADAS Project:

- HiL scripts automation validation on HiLs:
 - ADAS functionalities validation: ACC, AEB & LKA
 - ECU & environment validations: Diagnostics, Cybersecurity
- Video & Data Acquisition Assistance for HiL testing



• HiL Prototype Lab:

- HiL Validation: PoC of Cluster Calibration (Vector HiL, CANoe, VTestStudio);
- Simulation: CARSIM Simulator (VirtualCAE)
- Training, Events, Technologies:
 - Matlab/Simulink & Model Base Design (MDB);
 - XiL (HiL, SiL, MiL)
 - Automation: Vector (CANoe, CANalyzer), Python
 - Road to Zero Prototype (VI-Grade)





