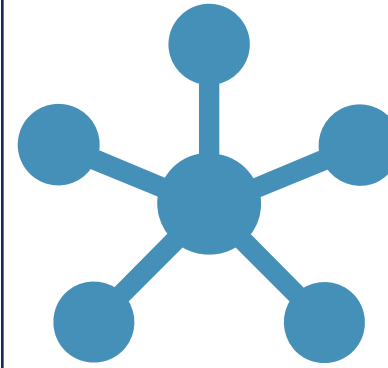


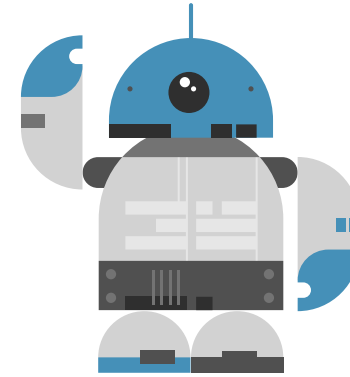


Xilinx – Zynq, Artix, Virtex, Kintex
 Altera – Cyclone, MAX series
 Microchip - PolarFire, ProASIC
 HW – SW Co-Verification,
 Emulation



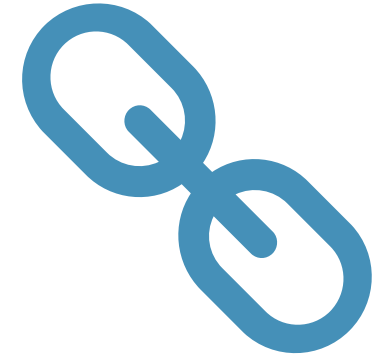
Complex
HW
System

PCB Design
(upto 12 layers)



FPGA

Algorithms Development
 Testing platform board
 R&D for AI/ML
 Verification & Prototype



Software &
Simulation

C, C++, QML,
 Multi-Die Package
 Package+PCB
 PCB level simulation (SI+PI)

STRENGTH (22 + YEAR EXPERIENCE IN FPGA)

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