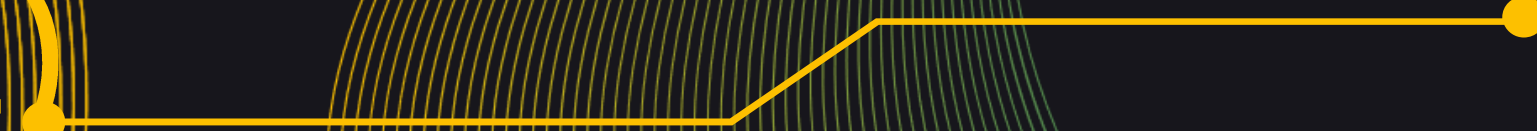


Logiicdev (SME)



Elena Mokina
Graz, 31.March 2025

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www.logiicdev.eu



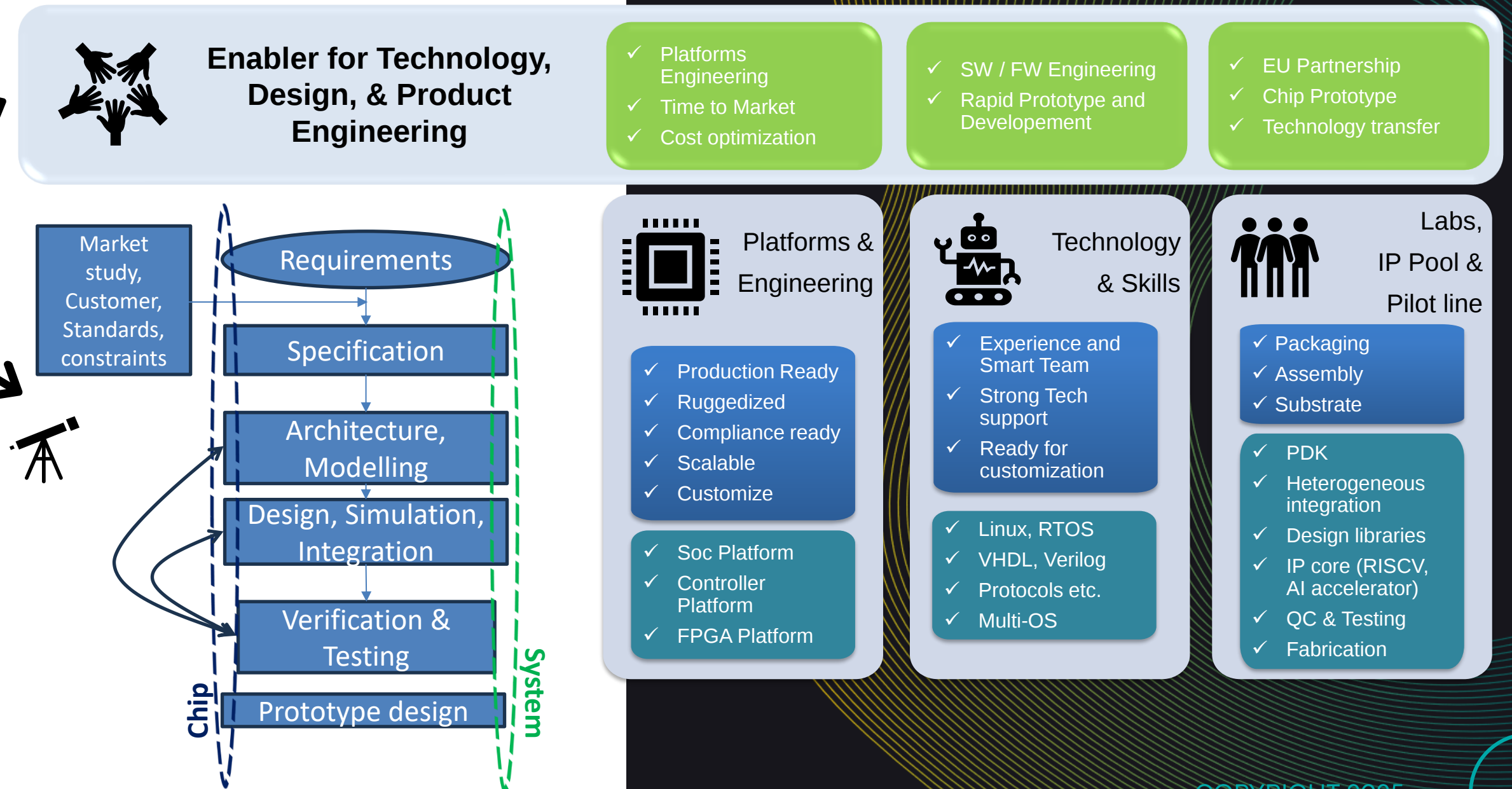
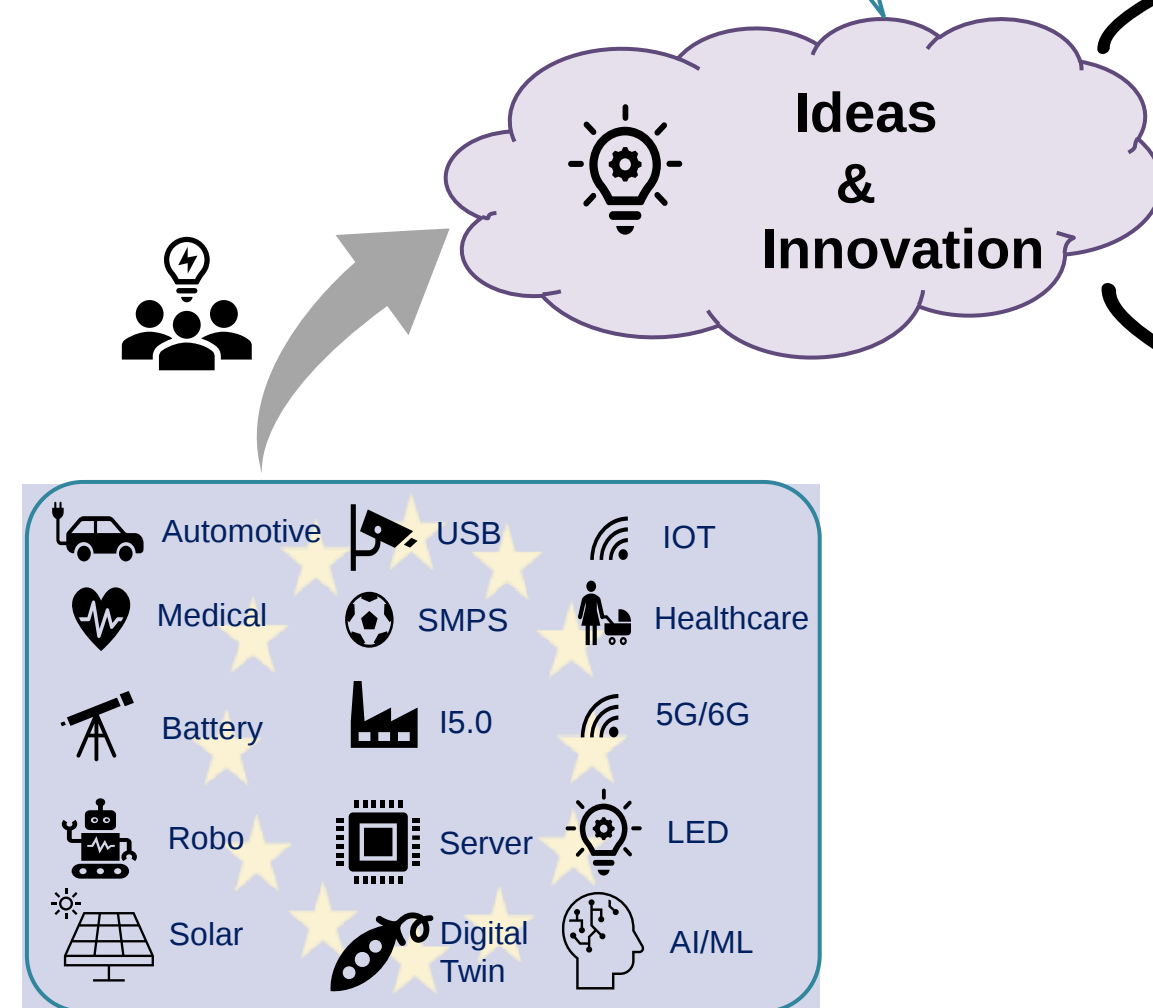
 Logiicdev GmbH

 Logiicdev

Semiconductor & Edge PLATFORM^^

- Right Fit Team and Skills
- Time to Market
- Execution and Product Cost
- Compliance and Certification
- Product developments & rigorous Testing

Key Propositions



Market

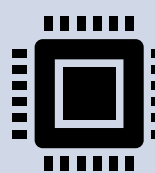
HW&SW POC, KPI PLATFORM

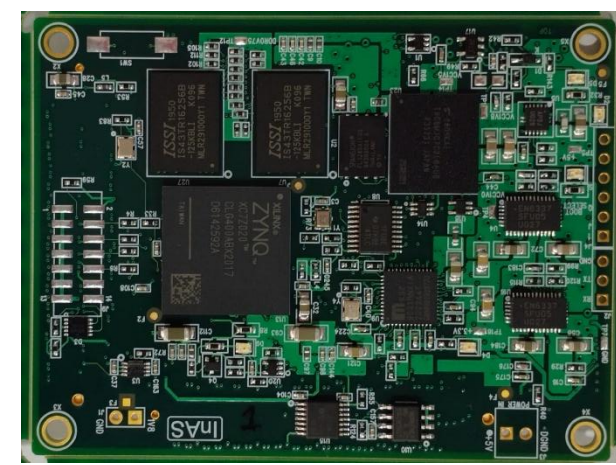


Enabler for higher TRL

- ✓ Platforms Engineering
- ✓ R&D Algorithm development
- ✓ Design optimization

- ✓ SW / FW Engineering
- ✓ Rapid Prototype and Development
- ✓ AI/ML models

-  Platforms & Engineering
- ✓ Customizable algorithms
 - ✓ Ruggedized Integration
 - ✓ Compliance ready
 - ✓ Scalable
 - ✓ Simulink FPGA
- ✓ Reconfigurable Platform
 - ✓ Controller Platform
 - ✓ FPGA Platform

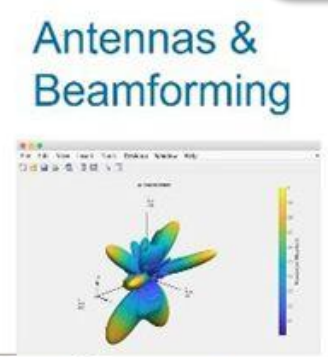
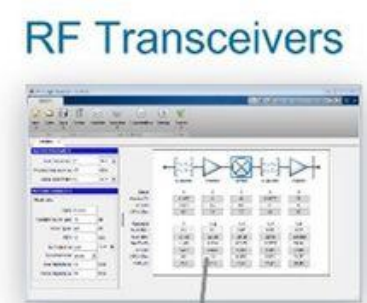
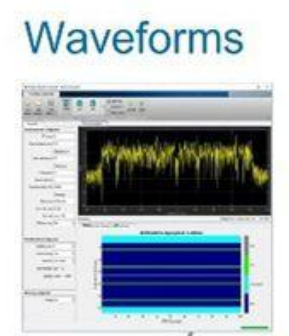


Algorithms

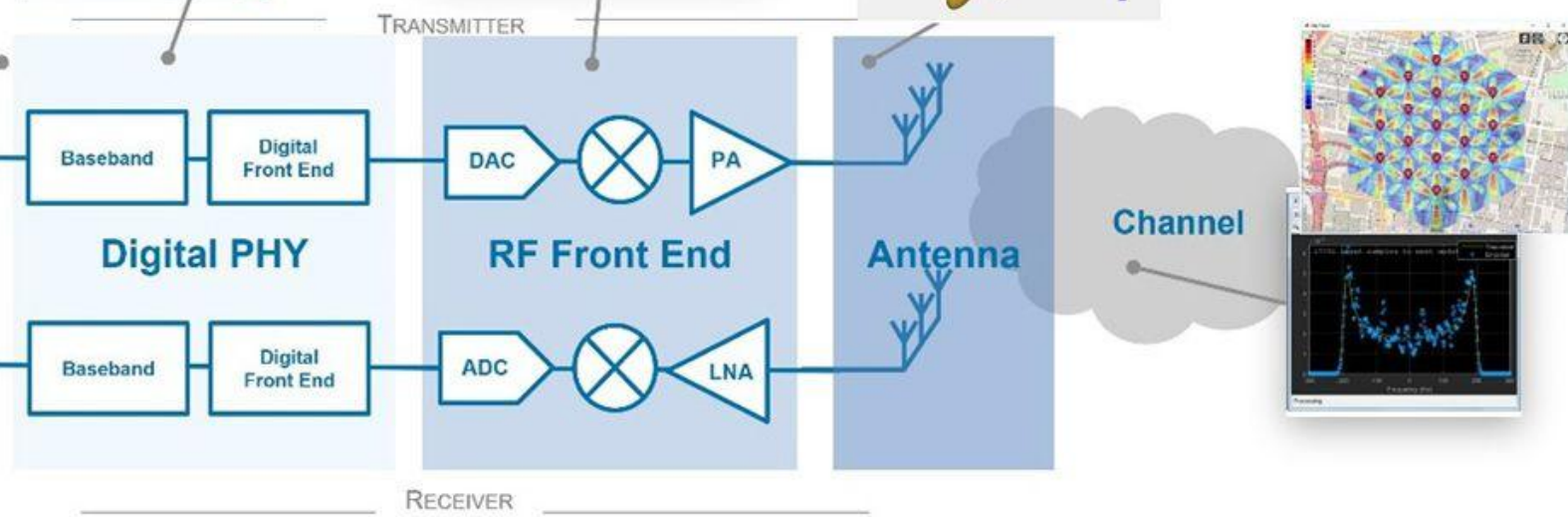
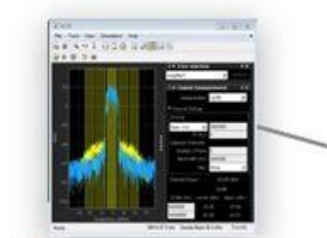
```

% Establish the number of component carriers.
numCC = length(NDLRB);

% Create transmission for each component carrier
enb = cell(1,numCC);
for i = 1:numCC
    enb(i) = ltermCDL('R,5');
    enb(i).NDLRB = NDLRB(i);
  
```



Test Signals



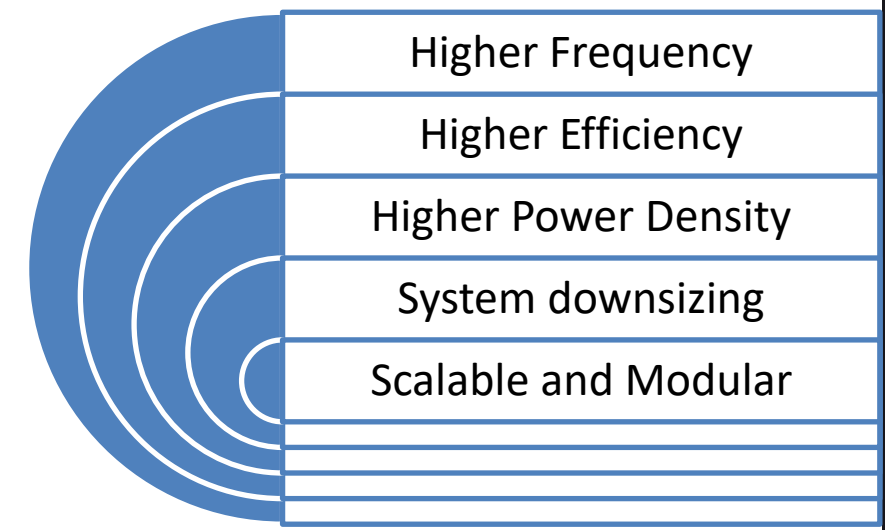
Your Valuable
Partner for

*Research
Fundings*

ASSETS (A1)

Omnipower,
Special motherboard
designed for power
application

Number of components: 1 400
Efficiency :91%
Number of pluggable board: 4
Reached TRL 5-6



(12) INTERNATIONAL APPLICATION PUBLISHED UNDER THE PATENT COOPERATION TREATY (PCT)
(19) World Intellectual Property Organization
International Bureau
(43) International Publication Date
28 December 2023 (28.12.2023)



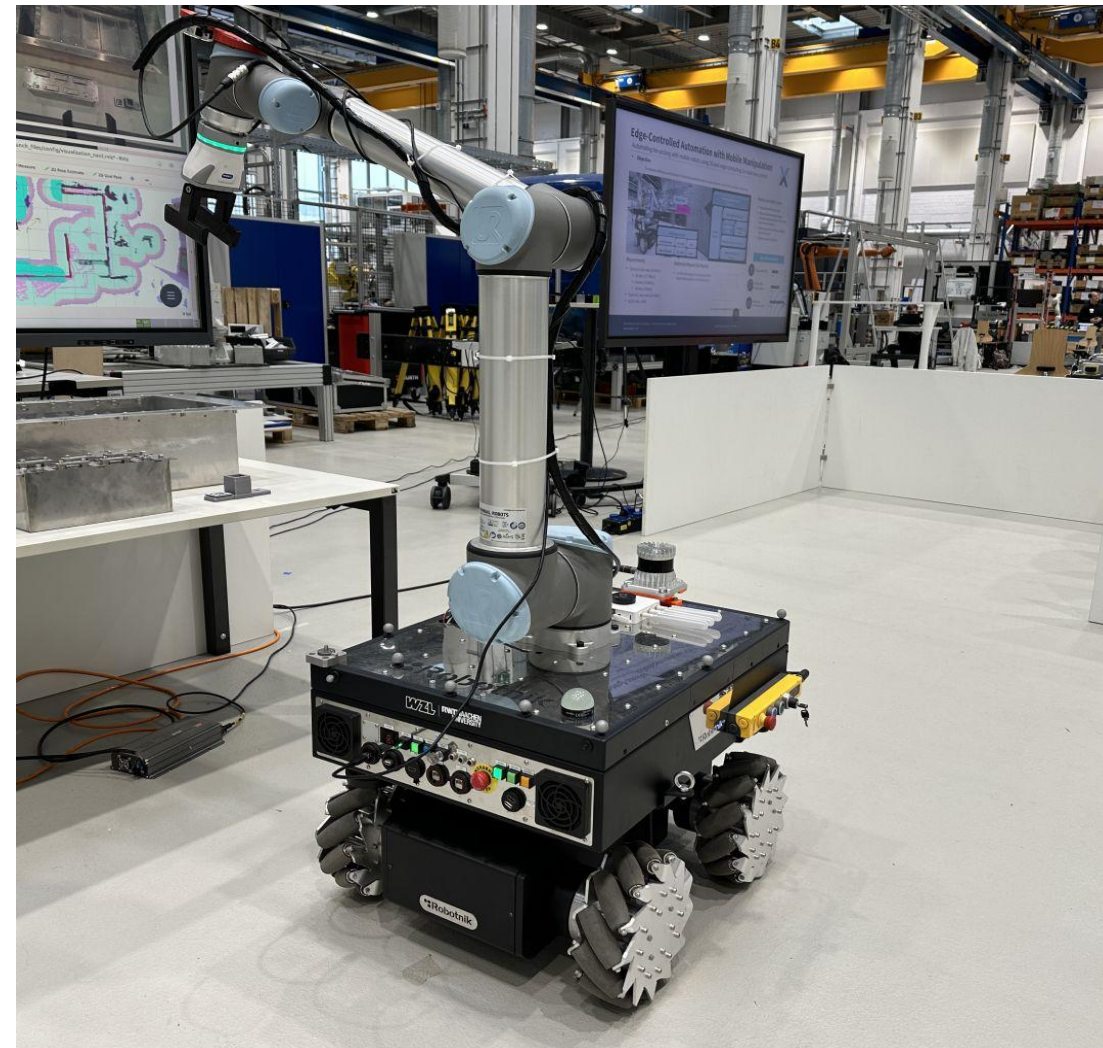
(10) International Publication Number
WO 2023/245214 A1

(51) International Patent Classification: G01R 31/40 (2020.01)	(72) Inventor: V KATKORIA, Deepak; Krottendorfer Str. 72/1, 8052 Graz (AT).
(21) International Application Number: PCT/AT2023/060138	(74) Agent: SCHWARZ & PARTNER PATENTANWÄLTE GMBH et al.; Wipplingerstrasse 30, 1010 Wien (AT).
(22) International Filing Date: 26 April 2023 (26.04.2023)	(81) Designated States (unless otherwise indicated, for every kind of national protection available): AE, AG, AL, AM, AO, AT, AU, AZ, BA, BB, BG, BH, BN, BR, BW, BY, BZ, CA, CH, CL, CN, CO, CR, CU, CV, CZ, DE, DJ, DK, DM, DO, DZ, EC, EE, EG, ES, FI, GB, GD, GE, GH, GM, GT, HN, HR, HU, ID, IL, IN, IQ, IR, IS, IT, JM, JO, JP, KE, KG, KH, KN, KP, KR, KW, KZ, LA, LC, LK, LR, LS, LU, LY, MA, MD, MG, MK, MN, MU, MW, MX, MY, MZ, NA, NG, NI, NO, NZ, OM, PA, PE, PG, PH, PL, PT, QA, RO, RS, RU, RW, SA, SC, SD, SE, SG, SK, SL, ST, SV, SY, TH, TJ, TM, TN, TR, TT, TZ, UA, UG, US, UZ, VC, VN, WS, ZA, ZM, ZW.
(25) Filing Language: English	
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(30) Priority Data: A 50446/2022 22 June 2022 (22.06.2022) AT	
(71) Applicant: LOGICDEV E.U. [AT/AT]; Krottendorfer Straße 72, 8052 Graz (AT).	



Eye twin for Robotic (A2)

- Hardware bridge platform Co-Robotic operation (CEA List)
- ROS2-based solution
- 5G SA connected
- AI/ML-based algorithm for vision control x3 Camera
- RISCv-based FPGA Accelerator
- AI Hailo Accelerator



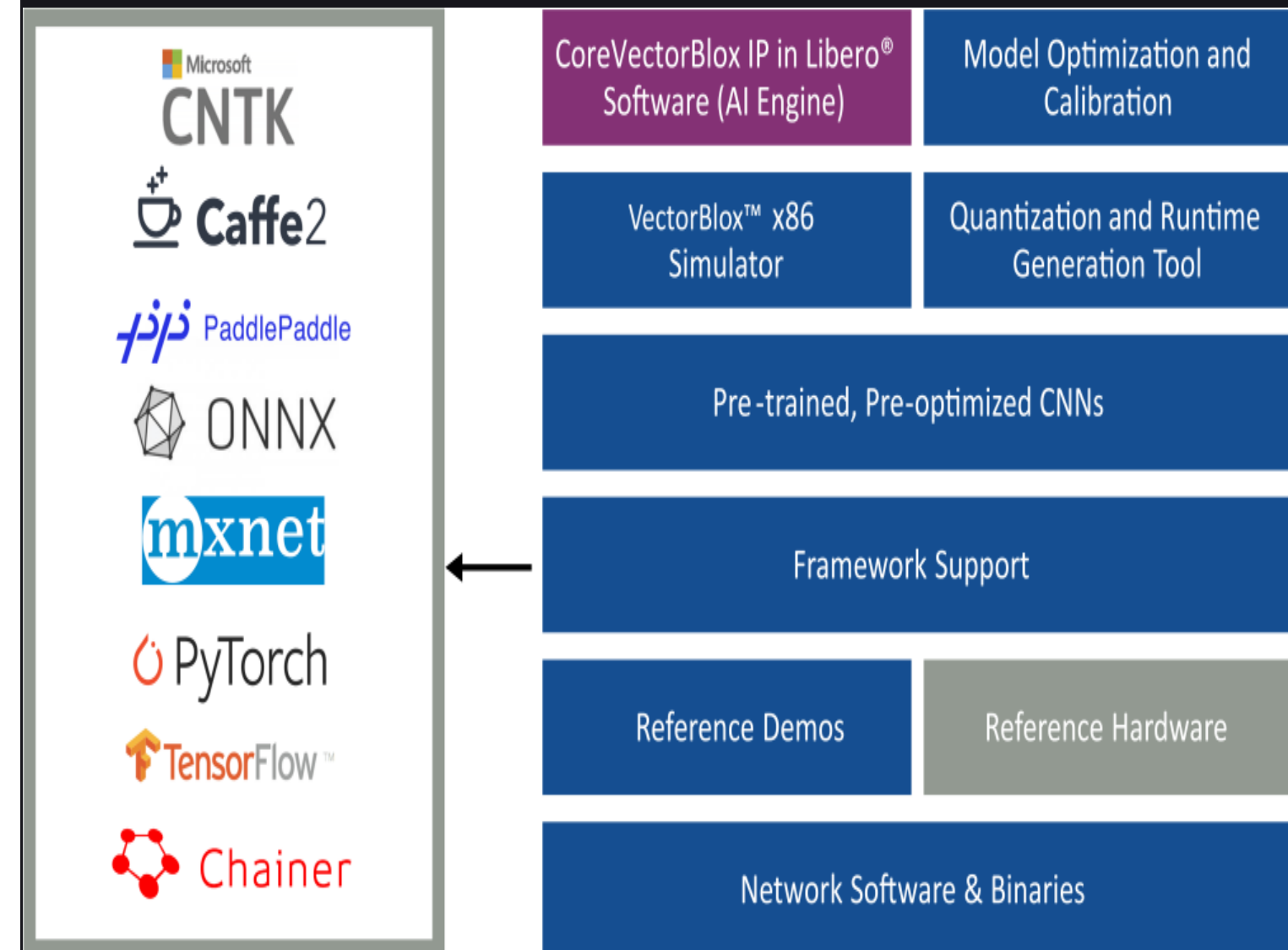
Robotic & Co-bot application (WZL der RWTH Aachen, DE)



Programmable Hardware solution



Other application



SDK & Framework

Team

Board of Founders



CEO & Founder
Deepak V Katkoria, M.Sc

- 20+ Years of Experience in the Industry
- Worked at Leading Positions (Intel, Freescale, BAE, NXP, Dialog and Infineon)
- R&D Management



Chief Advisor
Theo Hatzis, M.Sc

- 35+ Years of Experience in the Industry
- Self - Employed (Nürnberg, DE)
- Innovation Management
- Technical Consultant



Investor
Ian Macnamara

- 35+ Years of Experience in the Industry
- Investor
- Business Management

Team



Elena Mokina, B.Sc
Business Developer



Sravani Thota, MTech
Power Electronics
Engineer



Fedja Mulagebovic
AI - Hardware
Engineer



Priya A, B.Sc
AI Vision Developer

Board of Advisors



Jakob Udier
Sales Consultant



Darshna V Katkoria
Project Manager

Trusted by





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