EVRMIND
ENABLING THE AUTONOMOUS ECONOMY

evrmind.io · ibrahim@evrmind.io · +44 7446 065728

The Safety, Security and Assurance Chip for AI

Separate silicon that checks every AI action against your rules at runtime, blocks anything outside them, and records what it did, on hardware the AI can't reach or switch off.

SEPARATE SILICON
Enforcement the AI cannot reach.

EVERY ACTION CHECKED
Against your rules, at runtime.

SIGNED RECORD
Any tampering is detectable.

PROBLEM

- AI now takes real actions, and it can hallucinate, be attacked, or go rogue.
- Demand to automate is surging.
- Regulators are moving from testing the model to governing the act.
- The labs cannot promise to align their most capable systems.
- Today's agent controls all run in software.

WHY HARDWARE

- **The AI cannot reach it:** rules and keys sit on separate silicon.
- **Designed to hold** if the AI's host is compromised.
- **No grant, no action:** the command is withheld on the governed path.
- **Decides at machine speed,** before the action takes effect.
- **Works locally,** with no link to the cloud.
- **Every decision is tamper-evident** and signed in hardware.

MARKET AND BUYERS

TAM
$27.5bn
The addressable infrastructure opportunity by 2030.

SAM
$4.8bn
Regulated environments where control must be proven before deployment.

SOM
$60m
Defence and critical infrastructure first, then regulated finance.

Evrmind 2030 model; company estimate. Buyers are anyone who must prove control before they deploy (safety cases, JSP 936; NIS, CAF). Two routes: embedded via a prime or OEM, or a retrofit card into existing infrastructure.

TRACTION

- Engaged with a US autonomy prime, including briefs to its design team.
- Under review by a major EDA vendor's security IP team and a European national chip lab.
- Technical sessions with the UK's national cyber authority and a government secure-hardware programme.
- Engaged with a global asset manager and a major European bank at managing-director level, and with safety leadership at a leading AI chip maker.

TEAM

A team that can build it and sell it.

- **Ibrahim Vandenberg**, CEO, Chief Architect & Founder: award-winning salesperson who designed the architecture.
- **CTO**, signed: twenty years in silicon at Arm, Intel, Broadcom, Dialog and Imagination.
- **Erin Hales**, Lead Cryptographer, signed: ex-Optalysys.

STATUS Enforcement block running on commercial FPGA; full pipeline in development. 26 patent families filed at UKIPO; five papers published.

An AI infrastructure play: the potential standard for hardware-enforced AI safety, in every system that lets an AI act.

WHAT THE RAISE BUYS, OVER 18 MONTHS

1. First paid evaluations with primes, operators and OEMs.
2. Full pipeline validated on FPGA, with a design-partner demonstrator.
3. PCT filings, extending the 26 UK patent families internationally.
4. Seed-ready: first revenue and hardware in test.

PRE-SEED RAISE

GBP 3.5m

In three tranches: GBP 500k, 1m and 2m.
SEIS and KI-EIS advance assurance approved.
Ref WMBC/I&R/7167422036/VCRT

BUSINESS MODEL

Paid evaluation and integration, then hardware modules and retrofit cards, then licence and royalty at scale.

INVESTOR PACK