

Pillar 2

Session 2.8 Frugal innovation process

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Professor Romuald B. Beck, since his graduation from Warsaw University of Technology (WUT), whole his scientific carrier has been linked with Institute of Microelectronics and Optoelectronics at this University located in Warsaw, Poland.

His research interest is focused on issues related with silicon technology of both, semiconductor electronic and photonic devices as well as MEMS/MOEMS microsystems, especially – on fabrication of thin and ultrathin dielectric and conducting layers by different methods (including plasma techniques), their kinetics and modeling, application of plasma processes to ultra-shallow implantation, as well as electro-physical properties of semiconductor devices, their yield and reliability.

Research on all these topics is strongly dependent on access to high tech, specific in numerous ways, technological infrastructures.

Hence, his interest in conditions required for research and production clean-rooms and consequently - responsibility of building appropriate laboratories for teaching and research at WUT.



The experience gained then, and during many years of experimental research carried out in Poland, Belgium and USA, made him ideal candidate for head of experts team responsible for planning vision and mission, scientific profile as well as of designing laboratory area of newly established for 9 highly ranked in technical science Warsaw institutions (universities and institutes) organized in the form of consortium – Centre for Advanced Materials and Technologies - CEZAMAT.

After completion of its building process, he has taken a post of Vice-Director for Scientific Affairs of CEZAMAT and become responsible for putting the laboratories into operation and ramping up research activities.

In the meantime, Prof. Beck has been carrying several posts in national and international institutions, organizations and consortiums, e.g.: Committee of Electronics and Telecommunication of Polish Academy of Sciences, IEEE Section Poland, deputy to European Nanoelectronics Initiative Advisory Council ENIAC, European consortium SINANO Institute and Program or Technical Program Committees of numerous conferences (e.g.: ESSDERC).

