

200W isolated DC-DC converter Wide input and regulated single output



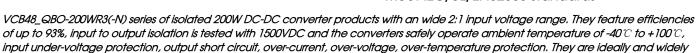


used in applications such as industrial control, electric power, instruments and communication fields.



- Wide 2:1 input voltage range
- High efficiency up to 93%
- I/O isolation test voltage 1.5k VDC
- Operating ambient temperature range: -40°C to +100°C
- Input under-voltage protection, output short circuit, over-current, over-voltage, over-temperature protection
- Industry standard package: 1/4 brick
- Meet IEC/UL/EN62368 standards





Selection	Guide						
	Part No.	Input Voltage (VDC)		Output		Full Load	
Certification		Nominal (Range)	Max. <sup>11</sup>	Voltage (VDC)	Current (A) Max./Min.	Efficiency <sup>©</sup> (%) Min./Typ.	Max. Capacitive Load(µF)
	VCB4805QBO-200WR3(-N)			5	36.00/0	89/91	6000
	VCB4812QBO-200WR3(-N)	48 (36-75)	75	12	16.67/0	91/93	2000
	VCB4815QBO-200WR3(-N)			15	13.33/0	91/93	2000
	VCB4824QBO-200WR3(-N)			24	8.33/0	90/92	1000

#### Notes:

- ① Exceeding the maximum input voltage may cause permanent damage;
- 2 Efficiency is measured in nominal input voltage and rated output load;
- ③"N" means negative logic.

Item	Operating Conditio	Min.	Тур.	Max.	Unit	
Input Current (full load / no-load)	Ni la -d la de la		4579/20	4682/35	4	
Reflected Ripple Current	Nominal input volta		30	120	mA	
Surge Voltage (1sec. max.)			-0.7	-	80	VDC
Start-up Voltage			_	_	36	VDC
Start-up Current			_	-	8.8	Α
Input Under-voltage Protection			26	29		VDC
Start-up Time	Nominal input voltage & constant resistance load		_	_	100	ms
Input Filter			Pi filter			
Hot Plug			Unavailable			
	N 41: -1	VCB48_QBO-200WR3	Ctrl pin open or pulled high (TL 3.5-12VDC)			DC)
	Module on	VCB48_QBO-200WR3-N	Ctrl pin pulled low to GND (0-1.2VDC)			
Ctrl <sup>®</sup>	N 41, -1	VCB48_QBO-200WR3	Ctrl pin pulled low to GND (0-1.2VDC)			
	Module off VCB48_QBO-200\		Ctrl pin open or pulled high (TL 3.5-12VDC)			
	Input current when off		-	3	10	mA

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Item	Operating Conditions		Min.	Тур.	Max.	Unit
Voltage Accuracy	0%-100% load			±1	±3	
Linear Regulation	Input voltage variation from lo	w to high at full load		±0.2	±0.5	%Vo
Load Regulation	5%-100% load			±0.5	±0.75	
Transient Recovery Time	25% load step change			200	500	μs
Town down Down and Down doubles	25% load step change 5V output Others	5V output		±3	±8	0() /-
Transient Response Deviation			±3	±7	%Vo	
Temperature Coefficient	Full load	Full load			±0.03	%/℃
Ripple & Noise <sup>®</sup>	20MHz bandwidth, nominal inpload	_	100	150	mV p-p	
Trim			90		110	20.4
Sense					105	%Vo
Over Temperature	Product surface max. tempero		130		°C	
Over-voltage Protection		110	125	160	%Vo	
Over-current Protection	Input voltage range		110	140	190	%lo
Short-circuit Protection		Continuous, self-recovery, time≤3 seconds				

Note: ①The "Tip and barrel" method is used for ripple and noise test, please refer to DC-DC Converter Application Notes for specific information. Ripple & Noise at <5% load is 5% Vo max.

Item	Operating Conditions	Min.	Тур.	Max.	Unit	
Isolation	Input-output Electric Strength test for 1 minute with a leakage current of 1mA max.				VDC	
Insulation Resistance	Input-output resistance at 500VDC	1000			ΜΩ	
Isolation Capacitance	Input-output capacitance at 100KHz/0.1V	-	1000	_	pF	
Operating Temperature	See typical characteristic curves	-40		+100	••	
Storage Temperature		-55	+125	+125	— င	
Storage Humidity	Non-condensing	5		95	%RH	
Die Calalania e Danishau	Wave soldering, 10 seconds			+260		
Pin Soldering Resistance Temperature	Soldering spot is 1.5mm away from case for 10 seconds			+300	င	
Shock and Vibration Test		10-150Hz, 10G, 30Min. along X, Y and Z				
Switching Frequency <sup>10</sup>	PWM Mode		200	-	KHz	
Altitude		Atm	Altitude:		)KPa	
MTBF	MIL-HDBK-217F@25℃	500			K hours	

Mechanical Specifications				
Dimensions	57.9 x 36.80 x 10.05mm			
Weight	43.6g(Typ.)			
Cooling Method	Natural convection or forced air convection			

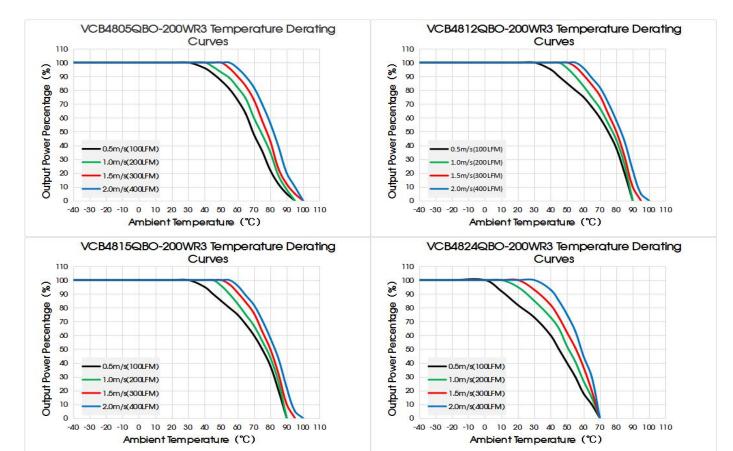
Electron	Electromagnetic Compatibility (EMC)						
Emissions	CE	CISPR32/EN55032	CLASS A (see Fig.3 for recommended circuit)/CLASS B (see Fig.	g.4 for recommended circuit)			
ETTISSIOTIS	RE	CISPR32/EN55032	CLASS A (see Fig.3 for recommended circuit)/CLASS B (see Fig.	g.4 for recommended circuit)			
	ESD	IEC/EN61000-4-2	Contact ±6KV	perf. Criteria B			
	RS	IEC/EN61000-4-3	10V/m	perf. Criteria A			
Immunity	EFT	IEC/EN61000-4-4	±2KV (see Fig.3 for recommended circuit)	perf. Criteria B			
	Surge	IEC/EN61000-4-5	line to line ±2KV(see Fig.3 for recommended circuit)	perf. Criteria B			
	CS	IEC/EN61000-4-6	3 Vr.m.s	perf. Criteria A			

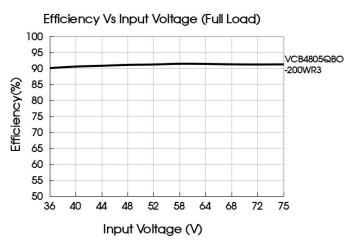
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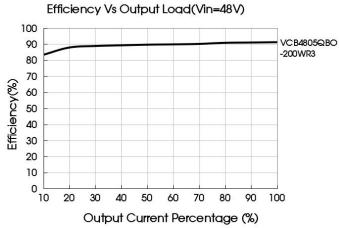
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# Typical Characteristic Curves

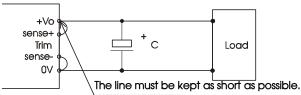






## Remote Sense Application

1. Remote Sense Connection if not used

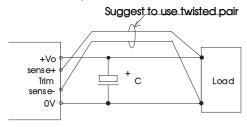


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#### Notes:

- (1) If the sense function is not used for remote regulation the user must connect the +Sense to + Vo and -Sense to 0V at the DC-DC converter pins and will compensate for voltage drop across pins only.
- (2) The connections between Sense lines and their respective power lines must be kept as short as possible, otherwise they may be picking up noise, interference and/or causing unstable operation of the power module.

## 2. Remote Sense Connection used for Compensation



#### Notes:

- (1) Using remote sense with long wires may cause unstable output, please contact technical support if long wires must be used.
- (2) PCB-tracks or cables/wires for Remote Sense must be kept as short as possible. Twisted pair or shielded wairs are suggested for remote compensation and must be kept as short as possible.
- (3) We recommend using adequate cross section for PCB-track layout and/or cables to connect the power supply module to the load in order to keep the voltage drop below 0.3V and to make sure the power supply's output voltage remains within the specified range.
- (4) Note that large wire impedance may cause oscillation of the output voltage and/or increased ripple. Consult technical support or factory for further advice of sense operation.

# Design Reference

## 1. Ripple & Noise

All the DC-DC converters of this series are tested before delivery using the recommended circuit shown in Fig. 1.

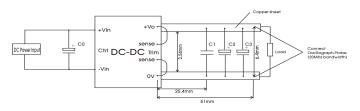


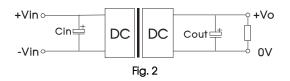
Fig. 1

Capacitors value Output voltage	CO	C1	C2	С3
5VDC				220uF/63 V
12VDC	100uF/10	1. 5 (50) (	10uF/50V	
15VDC	ov	1uF/50V		
24VDC				

### 2. Typical application

We recommended using Mornsun's EMC circuit, otherwise please ensure that at least a 100µF electrolytic capacitors is connected at the input in order to ensure adequate voltage surge suppression and protection.

Input and/or output ripple can be further reduced by appropriately increasing the input & output capacitor values Cin and Cout and/or by selecting capacitors with a low ESR (equivalent series resistance). Also make sure that the capacitance is not exceeding the specified max. capacitive load value of the product.



Capacitors value Output voltage	Cin	Cout	
5VDC			
12VDC	100	2200 15 (4.3) (	
15VDC	100uF/100V	220uF/63V	
24VDC			