

# Datasheet RS Pro K78xx-1000R3L DC-DC Converter

Wide input voltage non-isolated and regulated single output.

CECB Patent Protection RoHS



- High efficiency up to 96%
- No-load input current as low as 0.1mA
- Operating ambient temperature range -40 °C to +85 °C
- Output short-circuit protection
- Pin compatible with LM78xx series
- IEC60950, UL60950, EN60950 Approval
- 3 Year Warranty

K78xx-1000R3L series switching regulators are drop in replacements for LM78xx series three-terminal linear regulators. The high efficiency of these converters allows operation at full load without the need for a heat sink. With low ripple and standby power consumption these regulated converters are widely used in instrumentation, IoT and battery powered applications.

Selection Guide								
Certification	RS Stock no. RS Stock no. (Standard (Tube Pack Pack) 44pcs)		Part No.	Input Voltage (VDC) <sup>৩</sup>	Ou	tput	Full Load Efficiency (%)	Max. Capacitive
			Nominal (Range)	Voltage (VDC)	Current (mA) Max.	Vin Min. / Vin Max.	Load (μF)	
UL/CE/CB	1934013	1934012	K7803-1000R3	24 (6-36)	3.3	1000	90/81	680
	1934023	1934022	K7803-1000R3L	24 (6-36)	3.3	1000	90/81	680
	1934015	1934014	K7805-1000R3	24 (8-36)	5	1000	93/86	680
	1934025	1934024	K7805-1000R3L	24 (8-36)	5	1000	93/86	680
	1934017	1934016	K7809-1000R3	24 (13-36)	9	1000	95/90	680
	1934019	1934018	K7812-1000R3	24 (16-36)	12	1000	96/93	680
	1934028	1934026	K7812-1000R3L	24 (16-36)	12	1000	96/93	680
	1934021	1934020	K7815-1000R3	24 (18-36)	15	1000	96/94	680

Note:

Tor input voltage exceeding 30 VDC, an input capacitor of 22uF/50V is required;

② L-suffix: Add L-suffix for horizontal mount with 90 degree angled pins (K78xx-1000R3L).

# **DC/DC** Converter K78xx-1000R3(L) Series

Input Specifications					
Item	Operating Conditions	Min.	Тур.	Max.	Unit
No-load Input Current	Positive output		0.1	1	mA
Input Filter			Capacita	ance filter	

#### **Output Specifications** Unit Item **Operating Conditions** Min. Тур. Max. K7803-1000R3(L) --±2 ±4 Voltage Accuracy Full load, input voltage range ±2 Others --±3 % Linear Regulation Full load, input voltage range --±0.2 ±0.4 Load Regulation Nominal input,10% -100% load ±0.4 ±0.6 --Ripple & Noise\* 20MHz bandwidth, nominal input, 20% -100% load 20 75 -mVp-p **Temperature Coefficient** Operating ambient temperature -40°C to +85°C ±0.03 %/°C ----Transient Response Deviation 300 50 mV --Nominal input voltage, 25% load step change **Transient Recovery Time** 0.1 1 ms --Continuous, self-recovery Short-circuit Protection Nominal input

\*Note:1. The "parallel cable" method is used for Ripple and noise test, please refer to DC-DC Converter Application Notes for specific information; 2. With light loads at or below 20%, Ripple & Noise for 3.3/5V output parts increases to 100mVp-p max, and for 9V/12V/15V output parts to 2%Vo max.

<b>General Specification</b>	ons					
ltem	Operating Condition	Min.	Тур.	Max.	Unit	
Operating Temperature*	Derating if the tempe	rature ≥71°C (see Fig. 1)	-40		85	
Storage Temperature					125	℃
Pin Soldering Resistance Temperature	Soldering time: 10 seconds				260	
Storage Humidity	Non-condensing		5		95	%RH
Switching Frequency	100% load, inputK7803-1000R3voltage range-1000R3(L)Other output	K7803-1000R3(L)/K7805 -1000R3(L)	420	520	620	KHz
		Other output	580	680	780	
MTBF	MIL-HDBK-217F@25°C		2000			K hours

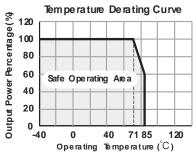
Note: \*The K7803-2000 (L) part requires an input voltage  $\geq$ 5V for operation at -40°C.

## **Mechanical Specifications**

Case Material		Black plastic; flame-retardant and heat-resistant (UL94 V-0)	
Dimensions	K78xx-1000R3	11.50 x 9.00 x 17.50 mm	
Dimensions	K78xx-1000R3L	19.00 x 11.50 x 9.00 mm	
Weight		3.8g (Тур.)	
Cooling Method		Free air convection	

Electromagnetic Compatibility (EMC)					
Emissions	CE	CISPR32/EN55032	CLASS B (see Fig. 4-2) for recommended circuit)		
Emissions	RE	CISPR32/EN55032	CLASS B (see Fig. 4-2) for recommended circuit)		
Immunity	ESD	IEC/EN 61000-4-2	Contact ±4KV	perf. Criteria B	
	RS	IEC/EN 61000-4-3	10V/m	perf. Criteria A	
	EFT	IEC/EN 61000-4-4	±1KV (see Fig. 4-① for recommended circuit)	perf. Criteria B	
	Surge	IEC/EN 61000-4-5	line to line ±1KV(see Fig. 4-① for recommended circuit)	perf. Criteria B	
	CS	IEC/EN 61000-4-6	3Vr.m.s	perf. Criteria A	

## **Typical Characteristic Curves**



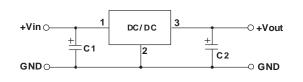


# DC/DC Converter

## K78xx-1000R3(L) Series

### **Design Reference**

#### 1. Typical application



Positive output application circuit

Fia. 2 Typi	cal applic	ation circ	cuit

table 1					
David Nia	C1	C2			
Part No.	(ceramic	(ceramic capacitor)			
K7803-1000R3(L)		22µF/10V			
K7805-1000R3(L)	10µF/50V	22µF/10V			
K7809-1000R3(L)		22µF/16V			
K7812-1000R3(L)		22µF/25V			
K7815-1000R3(L)		22µF/25V			

#### Note:

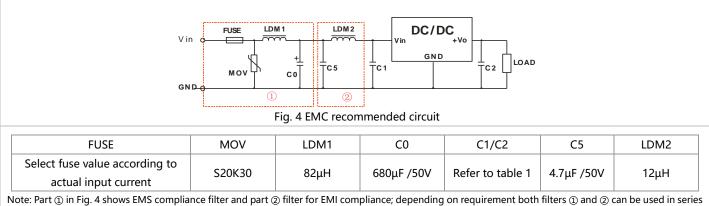
1. The required capacitors C1 and C2 must be connected as close as possible to the terminals of the module;

2. Refer to Table 1 for C1 and C2 capacitor values. For certain applications, increased value for C2 and/or tantalum or low ESR electrolytic capacitors may also be used instead;

3. When using configurations as shown in figure 3, we recommended to add an inductor (LDM) with a value of up to 10µH which helps reducing mutual interference;

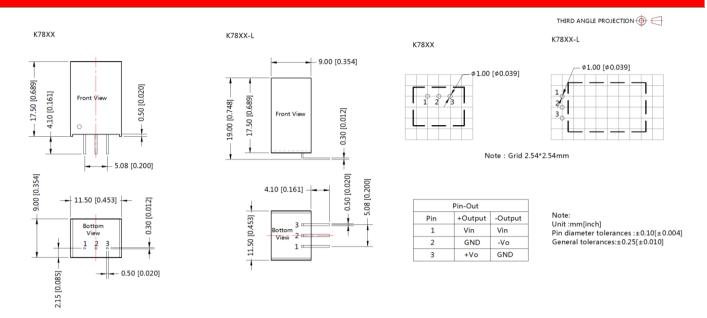
4. Converter cannot be used for hot swap and with output in parallel.

#### 2. EMC compliance circuit



Note: Part ① as shown.

## **Dimensions and Recommended Layout**



#### Notes:

- 1. The max. capacitive load should be tested within the input voltage range and under full load conditions;
- Unless otherwise specified, data in this data table should be tested under the conditions of Ta=25°C, humidity<75% when inputting nominal voltage and outputting rated load;
- 3. All index testing methods in this data table are based on our Company' s corporate standards;
- 4. We can provide product customization service, please contact our technicians directly for specific information;
- 5. Products are related to laws and regulations: see "Features" and "EMC";
- 6. Our products shall be classified according to ISO14001 and related environmental laws and regulations and shall be handled by qualified units.