



RoHS **Compliant**

Applications

• Suitable for Lighting, Switching Regulator and Motor Control

Features

- · With Built in Integrated Diode between Emitter & Collector
- This product is available in AEC-Q101 Compliant and PPAP Capable also.

Absolute Maximum Ratings (Ta = 25°C Unless otherwise specified)

Parame	ter	Symbol	Value	Unit
Collector Base Voltage		Vсво	700	
Collector Emitter (sus) Voltage		or Emitter (sus) Voltage VCEO 400		V
Emitter Base Voltage		VEBO	9]
Callagton Commant	Continuous	Ic	2	
Collector Current	Pulse ¹	Ісм	3]
0-11	Continuous	lв	0.75] , [
Collector Current	Pulse ¹	Івм	1.5	A
F:44 O	Continuous	le	2.25]
Emitter Current	Pulse ¹	Іем	4.5]
Power Dissipation @ Ta=2	25 °C	D-	1.4	W
Derate Above 25°C		P _D	11.2	mW/°C
Power Dissipation @ T₀=25 °C		Б	45	W
Derate Above 25°C		PD	360	mW/°C
Operating And Storage Junction Temperature Range		TJ, TSTG	-65 to +150	°C

Thermal Resistance

Parameter	Symbol	Value	Unit
Junction to Case	Rth (j-c)	2.77	°C/W
Junction to Ambient	Rth (j-a)	89	C/VV
Maximum Lead Temperature for Soldering Purpose: 1/8" from Case for 5 Seconds	Tι	275	°C

1. Pulse Test: Pulse Width=5ms, Duty Cycle=10%





Electrical Characteristics at (Ta = 25°C Unless otherwise specified)

Parameter	Symbol	Test Condition	Value			Unit
Parameter			Min.	Тур.	Max.	Oilit
Collector Base Voltage	Vсво	Ic=1mA, IE=0	700			V
Collector Emitter (sus) Voltage	VCEO(SUS)1	Ic=10mA, I _B =0	400			V
Callantan Cut Off Cumant	1	Vcb=680V, IE=0			1	
Collector Cut Off Current	Ісво	Vcb=680V, IE=0, Tc=100°C			5	mA
Emitter Cutoff Current	ІЕВО	V _{EB} =9V, IC=0			1	
DC Comment Coin	hFE²	Ic=500mA, VcE=5V2	15		25	
DC Current Gain	hfE	Ic=1A,VcE=5V	5]	25	
		Ic=0.5A, Iв=0.1A			0.5	
O-ll- dan Forittan O-tomation Valtana	VCE(sat) ¹	Ic=1A, Iв=0.25A]		1	
Collector Emitter Saturation Voltage		Ic=1.5A, Iв=0.5A			2.5	
		Ic=1A, Iв=0.25A,Tc=100°С	1		1	
		Ic=0.5A, Iв=0.1A			1	V
Base Emitter Saturation Voltage	V _{BE(sat)} ¹	Ic=1A, Iв=0.25A	[1.2	
		Ic=1A, Iв=0.25A,Tc=100°С	1		1.1	
Integrated Diode Forward Voltage	VFEC	I _F =2A	1		2	

Dynamic Characteristics

Parameter	Symbol	Test Condition	Value			Unit
Farameter	Symbol	rest Condition	Min.	Тур.	Max.	Unit
Current Gain Bandwidth Product	fτ	Ic=100mA, VcE=10V, f=1MHz	4			MHz
Output Capacitance	Сов	Vcв=10V, f=0.1MHz		21		pF

Switching Time

Parameter	Symbol	Toot Condition	Value			Unit
Farameter	Symbol Test Condition		Min.	Тур.	Max.	Offic
Turn On Time	ton	., ,			1.1	
Storage Time	tstg	Vcc=125V, lc=1A, l _{B1} =0.2A, l _{B2} =0.2A	2		4	μS
Fall Time	tr	161-0.271, 162-0.271			0.7	

1. Pulse Test:- PW=300µs, Duty Cycle=2%

hEE2 Classification:	А	В
hFE ² Classification:-	15-19	19-25



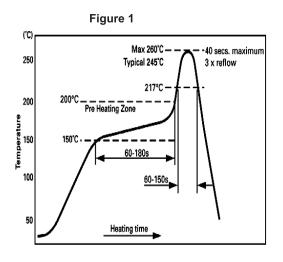


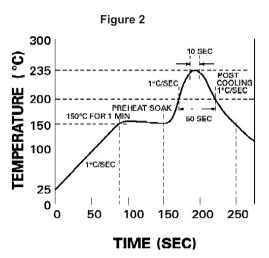
Recommended Reflow Solder Profiles

The recommended reflow solder profiles for Pb and Pb-free devices are shown below.

Figure 1 shows the recommended solder profile for devices that have Pb-free terminal plating, and where a Pb-free solder is used.

Figure 2 shows the recommended solder profile for devices with Pb-free terminal plating used with leaded solder, or for devices with leaded terminal plating used with a leaded solder.





Reflow profiles in tabular form

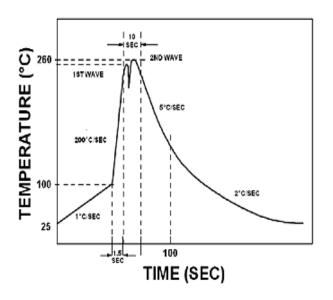
Profile Feature	Sn-Pb System	Pb-Free System
Average Ramp-Up Rate	~3°C/second	~3°C/second
Preheat – Temperature Range – Time	150-170°C 60-180 seconds	150-200°C 60-180 seconds
Time maintained above: - Temperature - Time	200°C 30-50 seconds	217°C 60-150 seconds
Peak Temperature	235°C	260°C max.
Time within +0 -5°C of actual Peak	10 seconds	40 seconds
Ramp-Down Rate	5°C/second max.	6°C/second max.

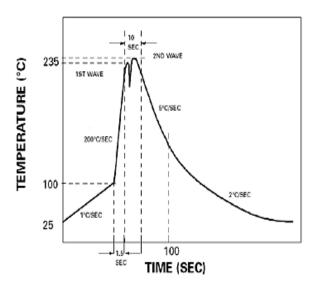


Recommended Wave Solder Profiles

The Recommended solder Profile For Devices with Pb-free terminal plating where a Pb-free solder is used

The Recommended solder Profile For Devices with Pb-free terminal plating used with leaded solder, or for devices with leaded terminal plating used with leaded solder





Wave Profiles in Tabular Form

Profile Feature	Sn-Pb System	Pb-Free System
Average Ramp-Up Rate	~200°C/second	~200°C/second
Heating rate during preheat	Typical 1-2, Max 4°C/sec	Typical 1-2, Max 4°C/Sec
Final preheat Temperature	Within 125°C of Solder Temp	Within 125°C of Solder Temp
Peak Temperature	235°C	260°C max.
Time within +0 -5°C of actual Peak	10 seconds	10 seconds
Ramp-Down Rate	3°C/second max.	5°C/second max.

Typical Characteristic Curves

Fig 1: Collector current v/s Collector Emitter Voltage

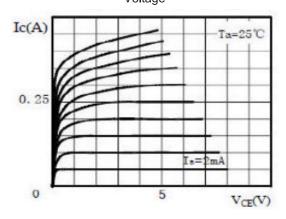


Fig 3:Collector emitter saturation voltage v/s Collector current

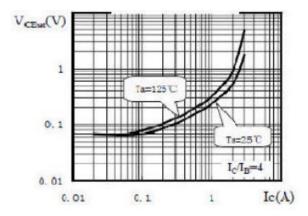


Fig 5: Power(%) v/s temperature

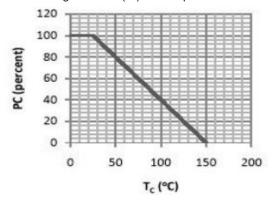


Fig 2:DC gain v/s Collector Current

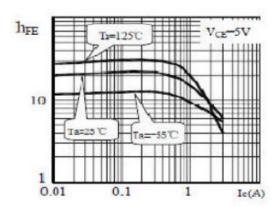


Fig 4. Base Emitter saturation voltage v/s collector current

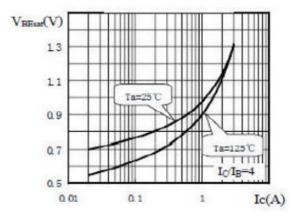
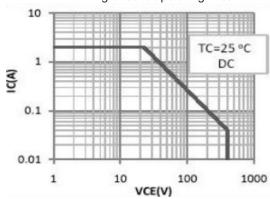


Fig 6: Safe operating Area

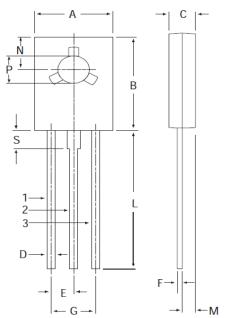






Dimensions

TO-126 Leaded Plastic Package



DIM	MIN.	MAX.		
А	7.4	7.8		
В	10.5	10.8		
С	2.4	2.7		
D	0.7	0.9		
Е	2.25 TYP			
F	0.49	0.75		
G	4.5 TYP			
L	15.7 TYP			
М	1.27 TYP			
N	3.75 TYP			
Р	3	3.2		
S	2.5 TYP			

Dimensions: Millimetres

Part Number Table

Description	Part Number	
Silicon Power Transistor, NPN, TO-126	CD13003D	

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