

RoHS

**Compliant** 

30W isolated DC-DC converter Ultra-wide input and regulated dual/single output



### r. ♣ CE CB Patent Protection

#### **Features**

- Ultra-wide 4:1 input voltage range
- High efficiency up to 90% with full load
- High efficiency up to 82% with 5% load
- No-load power consumption as low as 0.14W
- I/O isolation test voltage 1.5K VDC
- Input under-voltage protection, output shortcircuit, over-voltage, over-current protection
- Operating ambient temperature range: -40°C to +80°C
- Meets CISPR32/EN55032 CLASS A without extra components
- Meets EN50155 railway standard
- Six-sided metal shielded package
- IEC60950, UL60950, EN60950 approved
- Meets EN62368 standard

These series of isolated 30W DC-DC converter products with an ultra-wide 4:1 input voltage and feature efficiencies of up to 90%, input to output isolation is tested with 1500VDC and the converters safely operate ambient temperature of -40°C to +80°C. input under-voltage protection, output short-circuit, over-voltage, over-current protection. They meet CLASS A of CISPR32/ EN55032 EMI standards without external components, adding additional input reverse polarity protection and they are widely used in applications such as data transmission device, battery power supply device, telecommunication device, distributed power supply system, hybrid module system, remote control system, industrial robot and railway fields. Selection

Selection Guide								
		Input Volta	ge (VDC)	С	Output	Full Load Efficiency	Capacitive	
Certification	Part Number	Nominal (Range)	Max.	Voltage (VDC)	Current (mA) Max./Min.	(%) Min./Typ.	Load(µF)* Max.	
	MPRB2403LD-30W			3.3	6000/0	83/85	10000	
	MPRB2405LD-30W			5	6000/0	84/86	10000	
UL/CE/CB	MPRB2412LD-30W			12	2500/0	88/90	2700	
	MPRB2415LD-30W	0.4	40	15	2000/0	88/90	1680	
	MPRB2424LD-30W	24 (9 to 36)		24	1250/0	88/90	680	
	MPRA2405LD-30W			±5	±3000/0	84/86	2000	
CE	MPRA2412LD-30W			±12	±1250/0	87/89	1250	
CE	MPRA2415LD-30W			±15	±1000/0	87/89	680	
	MPRA2424LD-30W			±24	±625/0	87/89	470	
	MPRB4803LD-30W			3.3	6000/0	84/86	10000	
	MPRB4805LD-30W			5	6000/0	85/87	10000	
UL/CE/CB	MPRB4812LD-30W			12	2500/0	86/88	2700	
	MPRB4815LD-30W	48	80	15	2000/0	87/89	1680	
	MPRB4824LD-30W	(18 to 75)	00	24	1250/0	85/87	680	
	MPRA4805LD-30W			±5	±3000/0	84/86	2000	
CE	MPRA4812LD-30W			±12	±1250/0	86/88	1250	
	MPRA4815LD-30W			±15	±1000/0	86/88	680	

Use "H" suffix for heat sink mounting, "A2S" suffix for chassis mounting and "A4S" suffix for DIN-Rail mounting. We recommend to choose modules with a heat sink for enhanced heat dissipation and applications with extreme temperature requirements;

The minimum input voltage and starting voltage of A2S and A4S Model are 1VDC higher than those of DIP package due to input reverse polarity protection function;

Exceeding the maximum input voltage may cause permanent damage

Efficiency is measured at nominal input voltage and rated output load; efficiencies for A2S and A4S Model's is decreased by 2% due to the input reverse





Item	Operating Cond	itions	Min.	Тур.	Max.	Unit
		3.3V output		970/60	993/100	
	24VDC nominal input series, nominal input voltage	5V output		1454/60	1488/100	
Input Current (full load /	Thominal input voltage	Others		1388/6	1488/16	
no-load)	101/20	3.3V output		474/20	485/30	
	48VDC nominal input series, nominal input voltage	5V output		710/20	726/35	
	Thommal input voltage	Others		702/5	744/10	
Reflected Ripple Current	Nominal input voltage			40		
Curso Voltago (1000 may)	24VDC nominal input series	-0.7		50		
Surge Voltage(1sec. max.)	48VDC nominal input series	-0.7		100		
Ctart up Valtage	24VDC nominal input series			9		
Start-up Voltage	48VDC nominal input series			18		
Input Under voltage Protection	24VDC nominal input series	5.5	6.5			
Input Under-voltage Protection	48VDC nominal input series	12.0	15.5			
Start-up Time	Nominal input voltage & const	ant resistance load		10		
Input Filter			Pi filter			
Hot Plug				Unava	ilable	
	Module on		Ctrl pin op	en or pulle	d high (3.5-1	12VDC)
Ctrl *	Module off	Ctrl pin pulled low to GND (0-1.2VDC)				
	Input current when off		5	8	mA	

#### **Output Specifications**

Item	Operating Cor	nditions	Min.	Тур.	Max.	Unit
Valtaria Assurasi	5%-100% load			±1	±3	
Voltage Accuracy	0%-5% load			±1	±5	]
Linear Degulation	Input voltage variation from	Vo1		±0.2	±0.5	]
Linear Regulation	low to high at full load	Vo2		±0.5	±1	] <sub>%</sub>
Load Regulation	E0/ 4000/ load	Vo1		±0.5	±1	70
	5%-100% load	Vo2		±0.5	±1.5	
Cross Regulation	Dual output, Vo1 load at 50% of 10%-100%	6, Vo2 load at range			±5	
Transient Recovery Time	25% load step change, nomi	nal input voltage		300	500	μs
Transient Response Devia-	25% load step change,	3.3V/5V/±5V output		±5	±8	%
tion	nominal input voltage	Others		±3	±5	7%
Temperature Coefficient	Full load			±0.03	%/°C	
Pinnla & Naisa*	20MHz bandwidth, nominal	Singe output		50	100	m\/n n
Ripple & Noise*	input voltage, 100% load	Dual output		50	150	mVp-p





Item	Operating Conditions	Min.	Тур.	Max.	Unit
Trim			±10	-	%Vo
Over-voltage Protection	Input veltage range	110		160	70 00
Over-current Protection	Input voltage range	110		190	%lo
Short-circuit Protection		Hiccup, continuous, self-recovery			

Note: 1. Load regulation for 0%-100% load is ±5%;

<sup>2.</sup> The "parallel cable" method is used for ripple and noise test, please refer to DC-DC Converter Application Notes for specific information.

General Specifications							
Item	Operating Conditions	Min.	Тур.	Max.	Unit		
Isolation	Input-output Electric Strength test for 1 minute with a leakage current of 1mA max.	1500		VDC			
Insulation Resistance	Input-output resistance at 500VDC/60sec	1000	-	-	ΜΩ		
Isolation Capacitance	Input-output capacitance at 100KHz/0.1V	-	2000	-	pF		
Operating Temperature	See Fig. 1, Fig. 2, Fig. 3 and Fig. 4	2, Fig. 3 and Fig. 4 -40		+80	°C		
Storage Temperature		-55	-	+125			
Storage Humidity	Non-condensing	5	-	95	%RH		
Pin Soldering Resistance Temperature	Soldering spot is 1.5mm away from case for 10 seconds	-	-	+300	°C		
Vibration		IEC/EN61373 - Category 1, Grade B			Grade B		
Switching Frequency	PWM mode	-	300	-	kHz		
MTBF	MIL-HDBK-217F@25°C	1000	-	-	k hours		

Note:\* Switching frequency is measured at full load. The module reduces the switching frequency for light load (below 50%) efficiency improvement.

Mechanical Specifications							
Case Material	Aluminum alloy						
Dimensions	Horizontal package	(without heat sink)	50.8mm × 25.4mm × 11.8mm				
Weight	Without heat sink	Horizontal package/A2S chassis mounting/A4S Din-rail mounting	27.8g/52g/72g(Typ.)				
Cooling Method	Free air convection						



#### **Electromagnetic Compatibility (EMC)**

	0.5	Single output	CISPR32/EN55032	CLASS A (without external components)/ CLASS B (see Fig.6-2 for recommended circuit)	
Emissions -	CE	Dual output	CISPR32/EN55032	CLASS A (without external components)/ CLASS B (see Fig.7-2 for recommended circuit)	
	RE	Single output CISPR32/EN55032 CLASS A (without external components)/ CLASS B (see Fig.6-2 for recommended circui		CLASS A (without external components)/ CLASS B (see Fig.6-2 for recommended circuit)	
	KE	Dual output	CISPR32/EN55032	CLASS A (without external components)/ CLASS B (see Fig.7-2 for recommended circuit)	
	ESD		IEC/EN61000-4-2Co	perf. Criteria B	
	RS		IEC/EN61000-4-310	perf. Criteria A	
	EFT	Single output	IEC/EN61000-4-4±2KV (see Fig.6-1 for recommended circuit)		perf. Criteria B
Immunity		Dual output	IEC/EN61000-4-4±2	KV (see Fig.7-1 for recommended circuit)	perf. Criteria B
Immunity	Curao	Single output	IEC/EN61000-4-5 line	e to line ±2KV (see Fig.6-1for recommended circuit)	perf. Criteria B
	Surge	Dual output	IEC/EN61000-4-5 line	e to line ±2KV (see Fig.7-1for recommended circuit	)perf. Criteria B
	cs	Single output	IEC/EN61000-4-63 \	/r.m.s	perf. Criteria A
	03	Dual output	IEC/EN61000-4-610	Vr.m.s	perf. Criteria A

#### **Electromagnetic Compatibility (EMC) (EN50155)**

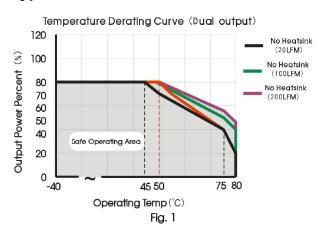
	٥٦	Single output	EN50121-3-2 150kHz-500kHz 99dBμV (see Fig.6-2 for recommended circuit) EN55016-2-1 500kHz-30MHz 93dBμV (see Fig.6-2 for recommended circuit)	
Footoniona	CE	Dual output	EN50121-3-2 150kHz-500kHz 99dBμV (see Fig.7-2 for recommended circuit) EN55016-2-1 500kHz-30MHz 93dBμV (see Fig.7-2 for recommended circuit)	
Emissions	DE	Single output	EN50121-3-2 30MHz-230MHz 40dBμV/m at 10m (see Fig.6-2 for recommended EN55016-2-1 230MHz-1GHz 47dBμV/m at 10m (see Fig.6-2 for recommended	
RE		Dual output	EN50121-3-2 30MHz-230MHz 40dBμV/m at 10m (see Fig.7-2 for recommended EN55016-2-1 230MHz-1GHz 47dBμV/m at 10m (see Fig.7-2 for recommended	
	ESD		EN50121-3-2 Contact ±6KV/Air ±8KV	perf. Criteria A
	RS		EN50121-3-2 20V/m	perf. Criteria A
	EFT	Single output	EN50121-3-2 ±2kV 5/50ns 5kHz (see Fig.6-1 for recommended circuit)	perf. Criteria A
		Dual output	EN50121-3-2 ±2kV 5/50ns 5kHz (see Fig.7-1 for recommended circuit)	perf. Criteria A
Immunity	Curao	Single output	EN50121-3-2 line to line $\pm 1 \text{KV}$ (42 $\Omega$ , 0.5 $\mu$ F) (see Fig.6-1 for recommended circui	t) perf. Criteria A
	Surge	Dual output	EN50121-3-2 line to line $\pm 1 \text{KV}$ (42 $\Omega$ , 0.5 $\mu$ F) (see Fig.7-1 for recommended circui	t) perf. Criteria A
	cs	Single output	EN50121-3-2 0.15MHz-80MHz 10V r.m.s	perf. Criteria A
	0.5	Dual output	EN50121-3-2 0.15MHz-80MHz 10V r.m.s	perf. Criteria A





# multicomp PRO

#### **Typical Performance Curves**



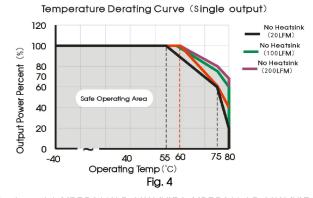
Apply model: MPRA2405LD-30W (H)R3 (9-18V input voltage)
MPRA2424LD-30W(H)R3 (9-18V input voltage)
MPRA4805LD-30W(H)R3 (18-36V input voltage)

120 No Heatsink (20LFM) € 100 No Heatsink (100**L**FM) Output Power Percent 80 No Heatsink (200LFM) 70 60 50 Safe Operating Area 40 20 0 75 80 50 55 -40 40 Operating Temp (°C)

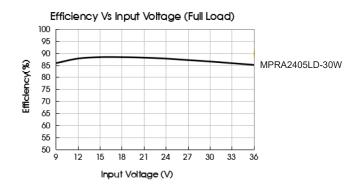
Temperature Derating Curve (Dual output)

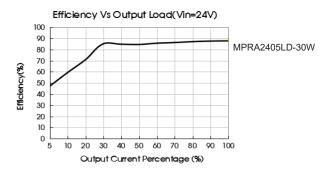
#### Temperature Derating Curve (Single output) 120 100 No Heatsink (100LFM) Output Power Percent (%) 80 No Heatsink 70 60 (200LEM) 50 Safe Operating Area 40 20 0 40 50 55 75 80 Operating Temp (°C) Fig. 3

Apply model: MPRB2403LD-30W (H)R3 MPRB2405LD-30W (H)R3 MPRB4803LD-30W (H)R3 MPRB4805LD-30W (H)R3



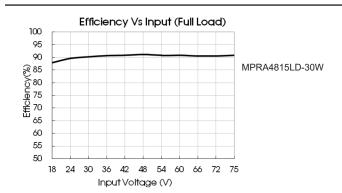
Apply model: MPRB2412LD-30W (H)R3 MPRB2415LD-30W (H)R3 MPRB2424LD-30W (H)R3 MPRA4812LD-30W (H)R3 MPRA4815LD-30W (H)R3 MPRB4824LD-30W (H)R3

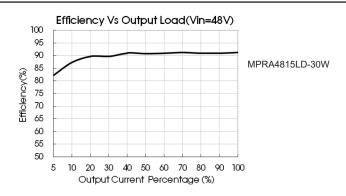






# multicomp PRO

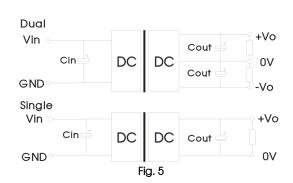




#### **Design Reference**

#### **Typical application**

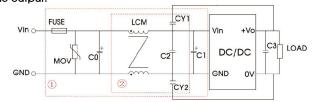
All the DC/DC converters of this series are tested before delivery using the recommended circuit shown in Fig. 5. Input and/or output ripple can be further reduced by appropriately increasing the input & output capacitor values Cin and Cout and/or by selecting capacitors with a low ESR (equivalent series resistance). Also make sure that the capacitance is not exceeding the specified max. capacitive load value of the product.



Single output voltage (VDC)	Cout (µF)	Cin (µF)	Dual output voltage (VDC)	Cout (µF)	Cin (µF)
3.3/5/9	220	100	±5/±12/±15	220	100
12/15/24	100	100	±24	100	100

#### **EMC** compliance circuit

#### Single output:



#### Parameter description:

Model	Vin:24VDC Vin:48VDC					
FUSE	Choose according to actual input current					
MOV	S20K30 S14K60					
C0	680µF/50V	330µF/100V				
C1	330µF/50V	330µF/100V				
C2	4.7µF/50V	2.2μF/100V				
C3	Refe	Refer to the Cout in Fig.5				
LCM	1mH, recommended to use MORNSUN's FL2D-30-102					
CY1/CY2	1nF/2KV					

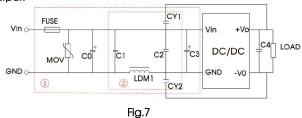
Notes: We use Part 1 in Fig. 6 for immunity and part 2 for emissions test.

Selecting based on needs.



# multicomp PRO

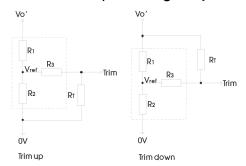
Dual output:



Notes:We use Part 1 in Fig. 7 for immunity and part 2 for emissions test. Selecting based on needs.

Model	Vin:24VDC	Vin:48VDC			
FUSE	Choose according to actual input current				
MOV	S20K30	S14K60			
C0	680µF/50V	330µF/100V			
C1/C2	2.2µF/50V	2.2µF/100V			
C3	330µF/50V 330µF/100				
C4	Refer to the Cout in Fig.5				
LDM1	3.3µH				
CY1/CY2	2.2nF/400VAC Safety Y Capacitor				

#### Trim Function for Output Voltage Adjustment (open if unused)



Calculating Trim resistor values:

up: 
$$RT = \frac{aR_2}{R_2 - a}$$
 -R3  $a = \frac{Vref}{Vo' - Vref}$  R1

down:  $RT = \frac{aR_1}{R_1 - a}$  -R3  $a = \frac{Vo' - Vref}{Vref}$  R2

R<sub>T</sub>= Trim Resistor value a= self-defined parameter Vo'=desired output voltage

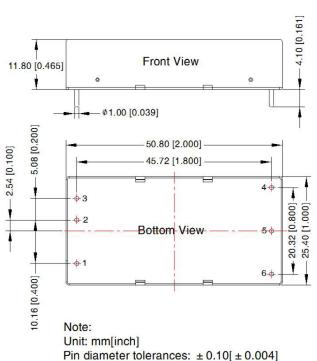
TRIM resistor connection (dashed line shows internal resistor network)

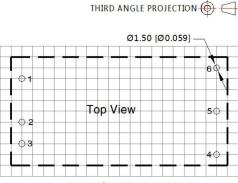
Vout(VDC)	R1(KΩ)	R2(KΩ)	R3(KΩ)	Vref(V)
3.3	4.801	2.87	12.4	1.24
5	2.883	2.87	10	2.5
9	7.500	2.87	15	2.5
12	11.000	2.87	15	2.5
15	14.494	2.87	15	2.5
24	24.872	2.87	17.8	2.5

The products do not support parallel connection of their output



#### Horizontal Package (without heat sink) Dimensions and Recommended Layout





Note: Grid 2.54\*2.54mm

Pin-Out	
Single	Dual
Ctrl	Ctrl
GND	GND
Vin	Vin
+Vo	+Vo
0V	OV
Trim	-Vo
	Single Ctrl GND Vin +Vo 0V

#### General tolerances: $\pm 0.50[\pm 0.020]$

#### Notes:

- 1. The maximum capacitive load offered were tested at input voltage range and full load;
- 3. Unless otherwise specified, parameters in this datasheet were measured under the conditions of Ta=25°C, humidity<75%RH with nominal input voltage and rated output load;
- 4. All index testing methods in this datasheet are based on company corporate standards;
- 5. We can provide product customization service, please contact our technicians directly for specific information;
- 6. Products are related to laws and regulations: see "Features" and "EMC";
- 7. Our products shall be classified according to ISO14001 and related environmental laws and regulations, and shall be handled by qualified units.

Important Notice: This data sheet and its contents (the "Information") belong to the members of the AVNET group of companies (the "Group") or are licensed to it. No licence is granted for the use of it other than for information purposes in connection with the products to which it relates. No licence of any intellectual property rights is granted. The Information is subject to change without notice and replaces all data sheets previously supplied. The Information supplied is believed to be accurate but the Group assumes no responsibility for its accuracy or completeness, any error in or omission from it or for any use made of it. Users of this data sheet should check for themselves the Information and the suitability of the products for their purpose and not make any assumptions based on information included or omitted. Liability for loss or damage resulting from any reliance on the Information or use of it (including liability resulting from negligence or where the Group was aware of the possibility of such loss or damage arising) is excluded. This will not operate to limit or restrict the Group's liability for death or personal injury resulting from its negligence. Multicomp Pro is the registered trademark of Premier Farnell Limited 2019.

