MAX38800 Evaluation Kit

Evaluates: MAX38800

General Description

The MAX38800 evaluation kit (EV kit) serves as a reference platform for evaluating the MAX38800 voltage regulator IC. This single-chip, integrated switching regulator provides an extremely compact, highly efficient, fast, accurate and reliable power delivery solution for low-output voltage applications. The MAX38800 has different programmability options to enable a wide range of configurations.

The EV kit consists of a fully-assembled and tested Printed Circuit Board (PCB) implementation of the MAX38800. Jumpers, test points, and input/output connectors are included for flexibility and ease-of-use. Refer to the data sheet for ordering information and more details.

Applications

- Servers/µServers
- I/O and Chipset Supplies
- GPU Core Supply
- DDR Memory—VDDQ and VTT
- Point-of-Load (PoL) Applications

***Ordering Information* appears at end of data sheet.**

Features

- High-Efficiency Solution
  - Up to 96% Peak
  - Up to 95.5% Full-Load
  - Up to 94% Light-Load Efficiency at 1A with DCM Enabled
- Inductor valley current limit is Configured to 7.5A (R_SEL = R1 = 2.67kΩ)
- Programmable Switching Frequency from 400kHz to 900kHz
- Programmable Positive and Negative OCP Limit
- Programmable Reference Voltage with External Input Option
- Fast Transient Response with Quick PWM™ Architecture
- Differential Remote Sense with Open-Circuit Detection
- Percentage-Based Output Power Good and OVP
- Open-Drain Status Indicator (STAT) Pin
- Input Undervoltage and Overvoltage Lockout
- Adaptive Dead Time Control
- Integrated Boost Switch
- 19-Bump WLCSP (2.2mm x 2.8mm) Footprint
- Operation Using Ceramic Input and Output Capacitors

319-100061; Rev 0; 8/17

maxim integrated™

Quick Start

Required Equipment

- MAX38800 EV kit
- 12V, 10A DC power supply
- Load capable of sinking 7.5A
- Digital voltmeter
- Oscilloscope

Procedure

The EV kit is fully assembled and tested. Follow the steps below to verify the board operation. **Caution: Do not turn on power supply until all connections are completed.**

1) Connect a 12V power supply to the VDD1 and GND1 banana jacks.
2) Make sure the shunt is installed on:
   a) J16 (1-2) to close the sense line.
   b) J4 (1-2) to power up the on-board LDO which regulates 1.8V.
   c) J12 (1-2) to provide the 1.8V bias supply to the regulator from the on-board LDO.
   d) J15 (3-5) to pull up the STAT pin.
   e) J15 (4-6) to pull up the OE pin.
3) Connect a voltmeter to the VOUT and GND banana jacks (J8, J11, J13, and J14 can be used as well).
4) Turn on the power supply.
5) Verify that the voltmeter reads 3.3V.

Detailed Description of Hardware

The MAX38800 provides compact, high-efficiency power delivery for precision outputs that demand fast transient response. The 19-ball (2.2mm x 2.8mm) CSP package minimizes the PCB area. The EV kit is preset for 3.3V output and can provide up to 7.5A from a 6.5V to 14V input supply.

Bias Supply

The MAX38800 EV kit has an on-board LDO (U2) that can provide the required 1.8V VCC bias voltage to both the regulator and pullup voltage for the Output Enable (OE) input. This allows testing the part using a single external power supply.

To enable the on-board LDO install the shunt on jumper J4. To effectively use the LDO to supply the VCC bias voltage to the regulator also install the shunt on jumper J12.

In order to properly measure the efficiency of the regulator, the LDO should not be active. The shunts on J4 and J12 need to be removed to disable the LDO. An external 1.8V, 0.1A current-limited power supply needs to be connected between J12-2 and ground. The same signal should be connected to J10 (1-2) to pull up the OE pin.

Regulator enable

To enable the regulator, OE pin needs to be pulled high. If the on-board 1.8V LDO is active (the shunt on jumper J4 is in place), the output voltage can be used for the purpose. Installing a shunt on J15 (4-6) pulls the OE signal high to 1.8V through a 20kΩ resistor. To shut down the regulator a shunt needs to be installed on J10. This forces the OE pin low.

Status Pin

The MAX38800 has an open collector status (STAT) output to report fault or output undervoltage event. Install a shunt on J15 (3-5) to pull up this pin to V_{CC} through a 20kΩ resistor. Since STAT pin is 3.3V tolerant, a shunt on J15 (1-3) can be installed to pull up this pin through a 20kΩ resistor to the 3.3V provided by the on board regulator U3 (install a shunt on J5 (3-4) to enable the LDO).

Scenario Selection

Several parameters of the MAX38800 can be programmed to allow optimization for specific applications. By selecting the appropriate value of resistor R_SEL (R1) and capacitor C_SEL (C4), the optimum set of parameters (scenario) can be programmed.

While R_SEL selects the proper scenario, C_SEL determines the nominal F_{SW}. The MAX38800 features a configuration table to provide a wide range of options. Table 1 shows the scenario table for MAX38800.

Setting the Output Voltage

The output voltage of MAX38800 depends both on the reference voltage (V_{REF}) and the resistor divider ratio.

Equation 1

$$V_{OUT} = V_{REF} \times \left(1 + \frac{R_6}{R_9}\right)$$

The reference voltage is selected through R_{SEL} (see Table 1) and can be either internal or external (refer to the data sheet for more details). In order to optimize the common mode rejection of the error amplifier, choose the voltage divider resistors so that their parallel resistance R_{PAR} is as close as possible to 2kΩ.

Equation 2

$$R_6 = V_{OUT} \times \left(\frac{R_{PAR}}{V_{REF}}\right)$$

$$R_9 = R6 \times \left(\frac{R_{PAR}}{R_6 - R_{PAR}}\right)$$

where,

R_6 = Top divider resistor

R_9 = Bottom divider resistor

R_{PAR} = Desired parallel resistance of R6 and R9

V_{OUT} = Output voltage

V_{REF} = Reference voltage

Operation with External V_{REF}

When using an external reference adopt the configuration shown in Figure 1. Once OE is asserted, the regulator briefly discharges the SENSE- node and releases it as regulation begins. In this case, the soft-start ramp is determined by the external low-pass filter time constant. The external filter time constant needs to be lower than $T_{SS}/3$ in order to avoid premature assertion of STAT pin while the output voltage is still ramping.

The external reference voltage can be applied prior to enabling the regulator, or ramped up right after enable is asserted. In both cases, the low-pass filtered reference voltage at SENSE- pin must reach its final value within T_{SS}.

Typical values for the filter components are:

- RF = 2.2kΩ
- CF = 0.22µF

Table 1. MAX38800 Configuration Table

R_SEL (kΩ)	V_{REF} (V)	SOFT-START TIME (T_{SS}) (ms)	VALLEY OCP INCEPTION (A)	OPERATION MODES	REPORTING (CURRENT/ TEMP)	R_{SENSE} (GAIN) (MΩ)	F_{SW} (kHz) C_SEL 0pF	F_{SW} (kHz) C_SEL 200 pF	F_{SW} (kHz) C_SEL 820 pF	T_{STAT} (µs)
1.78	0.95	6	6	CCM	Current	2.1	700	800	900	2000
2.67			7.5	CCM/DCM						
4.02		3	6	CCM						
6.04			7.5	CCM/DCM						
9.09	Ext.		6	CCM						
13.3		1.5								
20.0	0.6	6	9	CCM/DCM						
30.9				CCM						
46.4			6							
71.5				CCM/DCM	Temp					
107					Current	1.05				
162	Ext.	1.5	7.5	CCM	Temp	2.1	400	500	600	128

Input Voltage Monitoring

VDD1 and GND1 sense points as well as J3 can be used to monitor the input supply.

Output Voltage Monitoring

J11 and J13 monitor the output voltage. These test points should not be used for loading. Use scopejack J14 to monitor the output voltage ripple on an oscilloscope.

Efficiency Measurement

The following steps describe how to measure the regulator efficiency.

1) Connect a 12V power supply to the VDD1 and GND1 banana jacks. To avoid the input voltage to drop at high load due to power losses on connection cables connect the sense lines of the power supply to VDD1 and GND1 headers.
2) Connect an external 1.8V, 0.1A current limited power supply between J12-2 and ground.
3) Connect the same power supply to J10-1 to enable the regulator.
4) Connect a load to the VOUT and GND banana jacks for better results. J8 can also be used for low currents.
5) Make sure the shunt is installed on J16 (1-2) to close the sense line.
6) Remove all the other jumpers.
7) Connect a voltmeter to J11 or J13.
8) Turn on the power supply.
9) Measure V_{IN}, I_{IN}, V_{OUT}, I_{OUT}, V_{BIAS}, and I_{BIAS}.
10) Calculate the efficiency as:

Equation 3

$$\eta = \left(\frac{V_{OUT} \times I_{OUT}}{(V_{IN} \times I_{IN}) + (V_{BIAS} \times I_{BIAS})} \right)$$

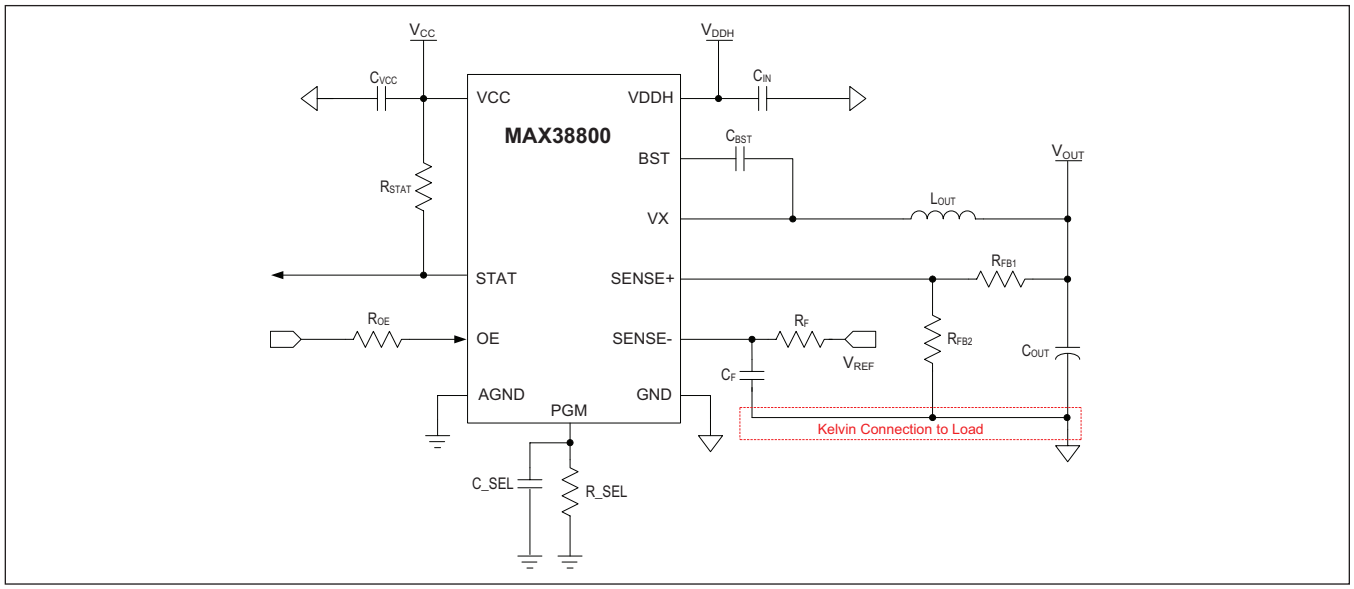


Figure 1. Electrical Connections to Use the External Voltage Reference Feature

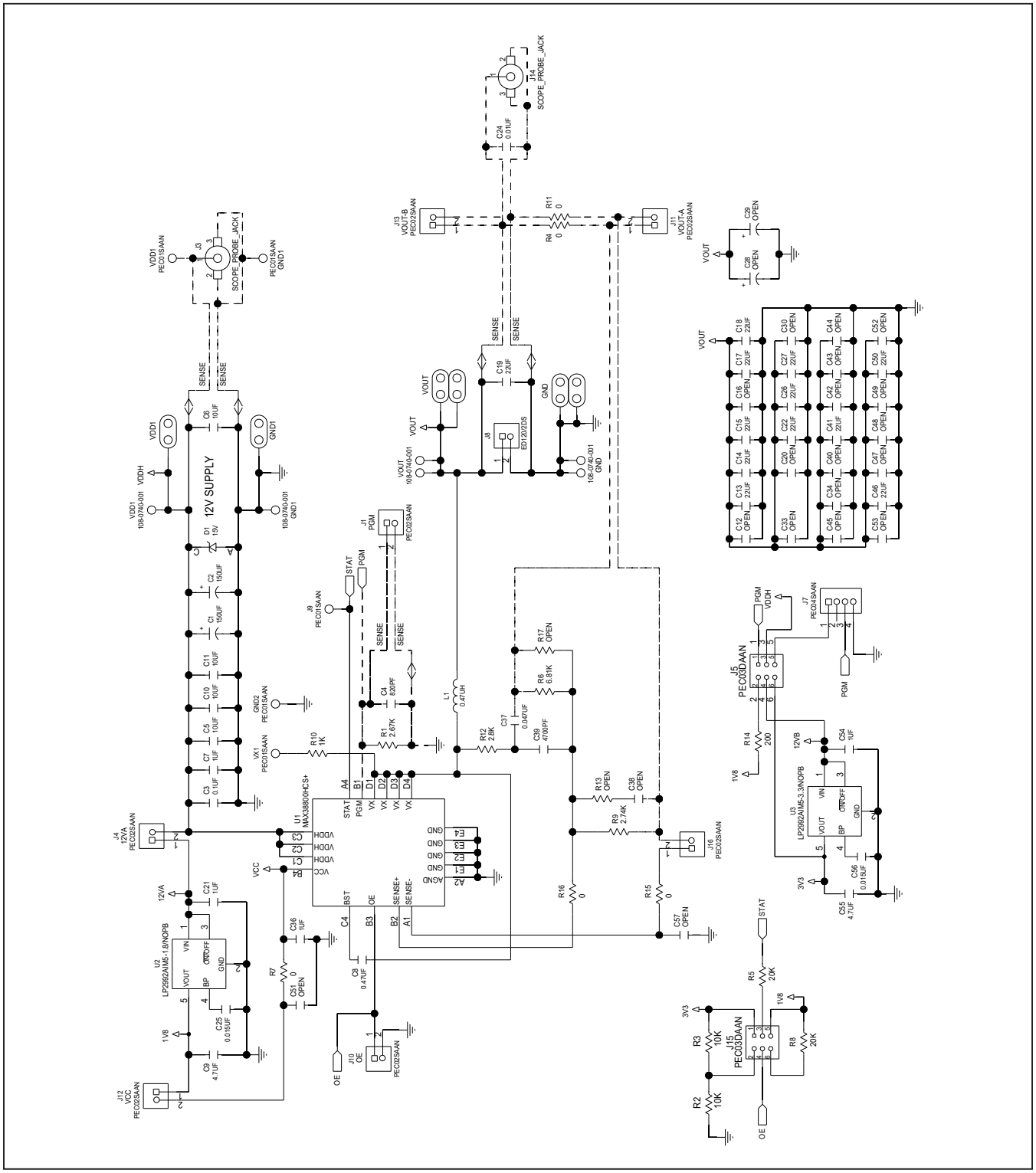
MAX38800 EV Kit Bill of Materials

ITEM	REF_DES	DNI/DNP	QTY	MFG PART #	MANUFACTURER	VALUE	DESCRIPTION
1	C1, C2	-	2	TPSE157M016R0100	AVX	150µF	CAPACITOR; SMT; 7343; TANTALUM; 150µF; 16V; 20%; TPS; -55°C to +125°C
2	C3	-	1	C0402X7R160-104KNE; CL05B104KO5NNNC; GRM155R71C104KA88; C1005X7R1C104K; CC0402KRX7R7BB104; EMK105B7104KV	VENKEL LTD./SAMSUNG ELECTRONICS/ MURATA/TDK/YAGEO PHICOMP /TAIYO YUDEN	0.1µF	CAPACITOR; SMT (0402); CERAMIC CHIP; 0.1µF; 16V; TOL = 10%; TG = -55°C TO +125°C; TC = X7R;
3	C4	-	1	ECJ-0EB1E821K	PANASONIC	820PF	CAPACITOR; SMT (0402); CERAMIC CHIP; 820PF; 25V; TOL = 10%; MODEL = ECJ SERIES; TG = -55°C TO +125°C; TC = X7R
4	C5, C6, C10, C11	-	4	C3216X7R1C106M160AC	TDK	10µF	CAPACITOR; SMT (1206); CERAMIC CHIP; 10µF; 16V; TOL = 20%; MODEL = C SERIES; TG = -55°C TO +125°C; TC = X7R
5	C7, C21, C54	-	3	EMK107B7105MA	TAIYO YUDEN	1µF	CAPACITOR; SMT (0603); CERAMIC CHIP; 1µF; 16V; TOL = 20%; MODEL = M SERIES; TG = -55°C TO +125°C; TC = X7R
6	C8	-	1	GRM188R71E474KA12	MURATA	0.47µF	CAPACITOR; SMT (0603); CERAMIC CHIP; 0.47µF; 25V; TOL = 10%; MODEL = GRM SERIES; TG = -55°C TO +125°C; TC = X7R
7	C9, C55	-	2	JMK105BBJ475MV-F; C1005X5R0J475M050BC	TAIYO YUDEN; TDK	4.7µF	CAPACITOR; SMT (0402); CERAMIC CHIP; 4.7µF; 6.3V; TOL = 20%; TG = -55°C TO +85°C; TC = X5R
8	C13-C15, C17-C19, C22, C26, C27, C41, C46, C50	-	12	C2012X6S0J226M125AB;GRM21BC80J	TDK/MURATA	22µF	CAPACITOR; SMT (0805); CERAMIC CHIP; 22µF; 6.3V; TOL = 20%; TG = -55°C TO +105°C; TC = X6S
9	C24	-	1	C0402C103K3RAC; GRM155R71E103KA01D; C1005X7R1E103K	KEMET; MURATA; TDK	0.01µF	CAPACITOR; SMT (0402); CERAMIC CHIP; 0.01µF; 25V; TOL = 10%; TG = -55°C TO +125°C; TC = X7R;
10	C25, C56	-	2	C0402X7R250-153KNE; GRM155R71E153KA61	VENKEL LTD./MURATA	0.015µF	CAPACITOR; SMT (0402); CERAMIC CHIP; 0.015µF; 25V; TOL = 10%; TG = -55°C TO +125°C; TC=X7R
11	C36	-	1	C0402X5R6R3-105KNP; C1005X5R0J105K; GRM155R60J105KE19; JMK105BJ105KV	VENKEL LTD./TDK/MURATA	1µF	CAPACITOR; SMT (0402); CERAMIC CHIP; 1µF; 6.3V; TOL = 10%; TG = -55°C TO +85°C; TC = X5R;
12	C37	-	1	C1005X7R1E473K; GRM155R71E473K	TDK/MURATA	0.047µF	CAPACITOR; SMT (0402); CERAMIC CHIP; 0.047µF; 25V; TOL = 10%; TG = -55°C TO +125°C
13	C39	-	1	TMK105BJ472KV-F	TAIYO YUDEN	4700PF	CAPACITOR; SMT (0402); CERAMIC CHIP; 4700PF; 25V; TOL = 10%; TG = -55°C TO +85°C; TC = X5R
14	D1	-	1	2EZ15D5	MICRO COMMERCIAL COMPONENTS	15V	DIODE; ZNR; THROUGH HOLE-AXIAL LEAD (DO-41); VZ = 15V; IZ = 0.122A
15	TP1-TP3, GND1, VDD1, VOUT	-	6	108-0740-001	EMERSON NETWORK POWER	108-0740-001	CONNECTOR; MALE; PANELMOUNT; BANANA JACK; STRAIGHT; 1PIN
16	J9, VX1, GND2, GND1_HEADER, VDD1_HEADER	-	5	PEC01SAAN	SULLINS ELECTRONICS CORP	PEC01SAAN	CONNECTOR; MALE; THROUGH HOLE; BREAKAWAY; STRAIGHT; 1PIN
17	J2, J6, GND_MAXIMPAD, GND1_MAXIMPAD, VDD1_MAXIMPAD, VOUT_MAXIMPAD	-	6	MAXIMPAD	N/A	MAXIMPAD	EVK KIT PARTS; MAXIM PAD; NO WIRE TO BE SOLDERED ON THE MAXIMPAD
18	J1, J4, J10-J13, J16	-	7	PEC02SAAN	SULLINS	PEC02SAAN	CONNECTOR; MALE; THROUGH HOLE; BREAKAWAY; STRAIGHT; 2PINS

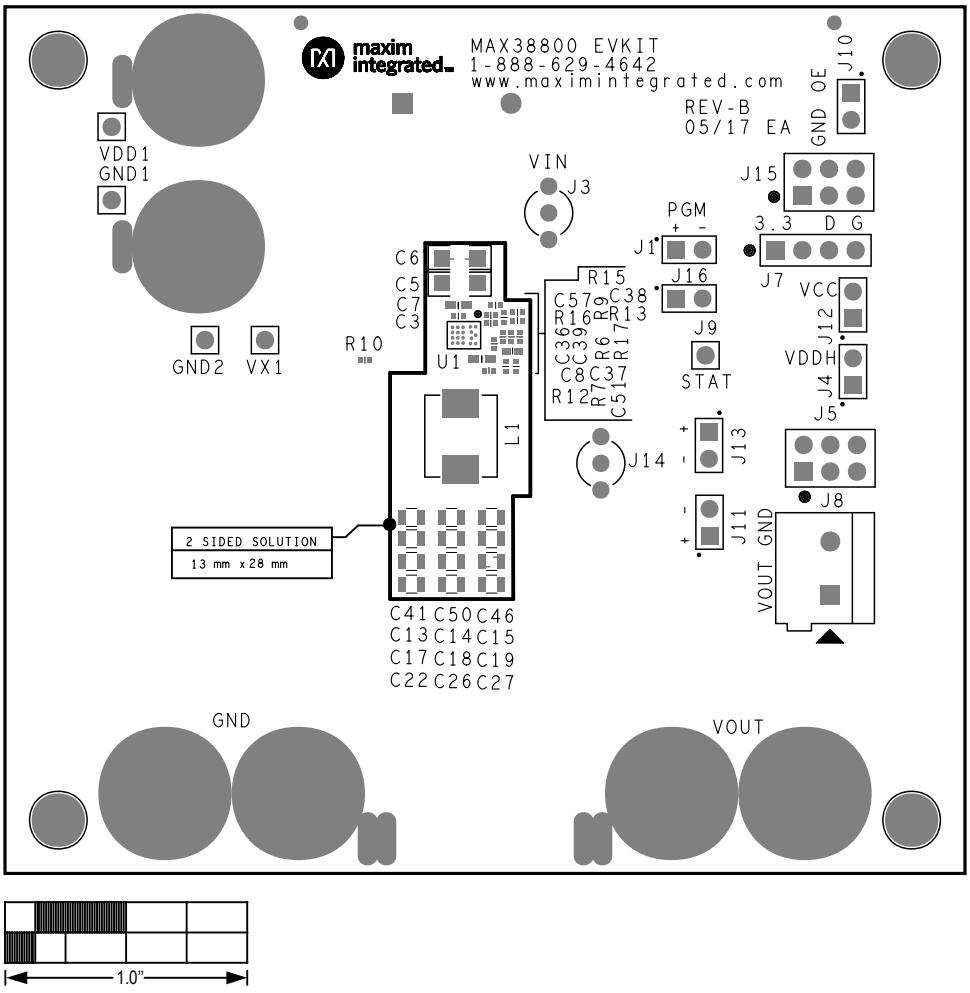
MAX38800 EV Kit Bill of Materials (continued)

ITEM	REF_DES	DNI/DNP	QTY	MFG PART #	MANUFACTURER	VALUE	DESCRIPTION
19	J3, J14	-	2	SCOPE_PROBE_JACK	MAXIM	SCOPE_PROBE_JACK	EVKIT PART-SCOPE_PROBE_JACK
20	J5, J15	-	2	PEC03DAAN	SULLINS ELECTRONICS CORP.	PEC03DAAN	CONNECTOR; MALE; THROUGH HOLE; BREAKAWAY; STRAIGHT THROUGH; 6PINS; -65°C TO +125°C
21	J7	-	1	PEC04SAAN	SULLINS ELECTRONICS CORP.	PEC04SAAN	CONNECTOR; MALE; THROUGH HOLE; BREAKAWAY; STRAIGHT; 4PINS
22	J8	-	1	ED120/2DS	ON-SHORE TECHNOLOGY INC.	ED120/2DS	CONNECTOR; FEMALE; THROUGH HOLE; BLUE TERMINAL BLOCK; STRAIGHT; 2PINS
23	L1	-	0	7.44373E+11	WURTH ELECTRONICS INC	0.47µH	INDUCTOR; SMT; WIREWOUND CHIP; 0.47µH; TOL = ±20%; 11.5A
24	L1	-	1	GLMCR4703A	ALPS	0.47µH	INDUCTOR; SMT; LIQUALLOY; 0.47µH; TOL = ±20%; 16.2A
25	R1	-	1	ERJ-2RKF2671X	PANASONIC	2.67K	RESISTOR; 0402; 2.67KΩ; 1%; 100PPM; 0.10W; THICK FILM
26	R2, R3	-	2	CRG0402F10K	TE CONNECTIVITY	10K	RESISTOR; 0402; 10KΩ; 1%; 100PPM; 0.063W; THICK FILM
27	R4, R7, R11, R15, R16	-	5	RC0402JR-070RL; CR0402-16W-000RJT	YAGEO PHYCOMP/VENKEL LTD.	0	RESISTOR; 0402; 0Ω; 5%; JUMPER; 0.063W; THICK FILM
28	R5, R8	-	2	ERJ-2GEJ203X	PANASONIC	20K	RESISTOR; 0402; 20KΩ; 5%; 200PPM; 0.10W; THICK FILM
29	R6	-	1	CRCW04026K81FK	VISHAY DALE	6.81K	RESISTOR; 0402; 6.81KΩ; 1%; 100PPM; 0.063W; METAL FILM
30	R9	-	1	CRCW04022K74FK	VISHAY DALE	2.74K	RESISTOR; 0402; 2.74K; 1%; 100PPM; 0.0625W; THICK FILM
31	R10	-	1	CRCW04021K00JK	VISHAY DALE	1K	RESISTOR; 0402; 1KΩ; 5%; 100PPM; 0.063W; METAL FILM
32	R12	-	1	ERA-2AEB2801X	PANASONIC	2.8K	RESISTOR; 0402; 2.8KΩ; 0.1%; 25PPM; 0.063W; METAL FILM
33	R14	-	1	RCC-0402PW200RF	INTERNATIONAL MANUFACTURING SERVICE	200	RESISTOR; 0402; 200Ω; 1%; 100PPM; 0.080W; THICK FILM
34	SU1-SU5	-	5	STC02SYAN	SULLINS ELECTRONICS CORP.	STC02SYAN	TEST POINT; JUMPER; STR; TOTAL LENGTH = 0.256IN; BLACK; INSULATION = PBT CONTACT = PHOSPHOR BRONZE; COPPER PLATED TIN OVERALL
35	U1	-	1	MAX38800HCS+	MAXIM	MAX38800HCS+	EVKIT PART-IC; VREG; INTEGRATED; STEP-DOWN SWITCHING REGULATOR; CSP19
36	U2	-	1	LP2992AIM5-1.8/NOPB	TEXAS INSTRUMENTS	LP2992AIM5-1.8/NOPB	IC; VREG; MICROPOWER 250-mA LOW-NOISE ULTRALOW-DROPOUT REGULATOR DESIGNED FOR USE WITH VERY LOW-ESR OUTPUT CAPACITOR; SOT23-5
37	U3	-	1	LP2992AIM5-3.3/NOPB	TEXAS INSTRUMENTS	LP2992AIM5-3.3/NOPB	IC; VREG; MICROPOWER 250-mA LOW-NOISE ULTRALOW-DROPOUT REGULATOR DESIGNED FOR USE WITH VERY LOW-ESR OUTPUT CAPACITOR; SOT23-5
38	PCB	-	1	MAX38800	MAXIM	PCB	PCB:MAX38800
39	C12, C16, C20, C30, C33, C34, C40, C42-C45, C47-C49, C52, C53	DNP	0	N/A	N/A	OPEN	PACKAGE OUTLINE 0805 NON-POLAR CAPACITOR
40	C28, C29	DNP	0	N/A	N/A	OPEN	PACKAGE OUTLINE 7343 HEIGHT 4.3MM ELECTROLYTIC CAPACITOR
41	C38, C51, C57	DNP	0	N/A	N/A	OPEN	PACKAGE OUTLINE 0402 NON-POLAR CAPACITOR
42	R13, R17	DNP	0	N/A	N/A	OPEN	PACKAGE OUTLINE 0402 RESISTOR
43	C4	DNP	0	C0402C821K5RAC; GRM155R71H821KA01	KEMET/MURATA	820PF	CAPACITOR; SMT (0402); CERAMIC CHIP; 820PF; 50V; TOL = 10%; TG = -55°C TO +125°C; TC = X7R
TOTAL			88				

MAX38800 EV Kit Schematic

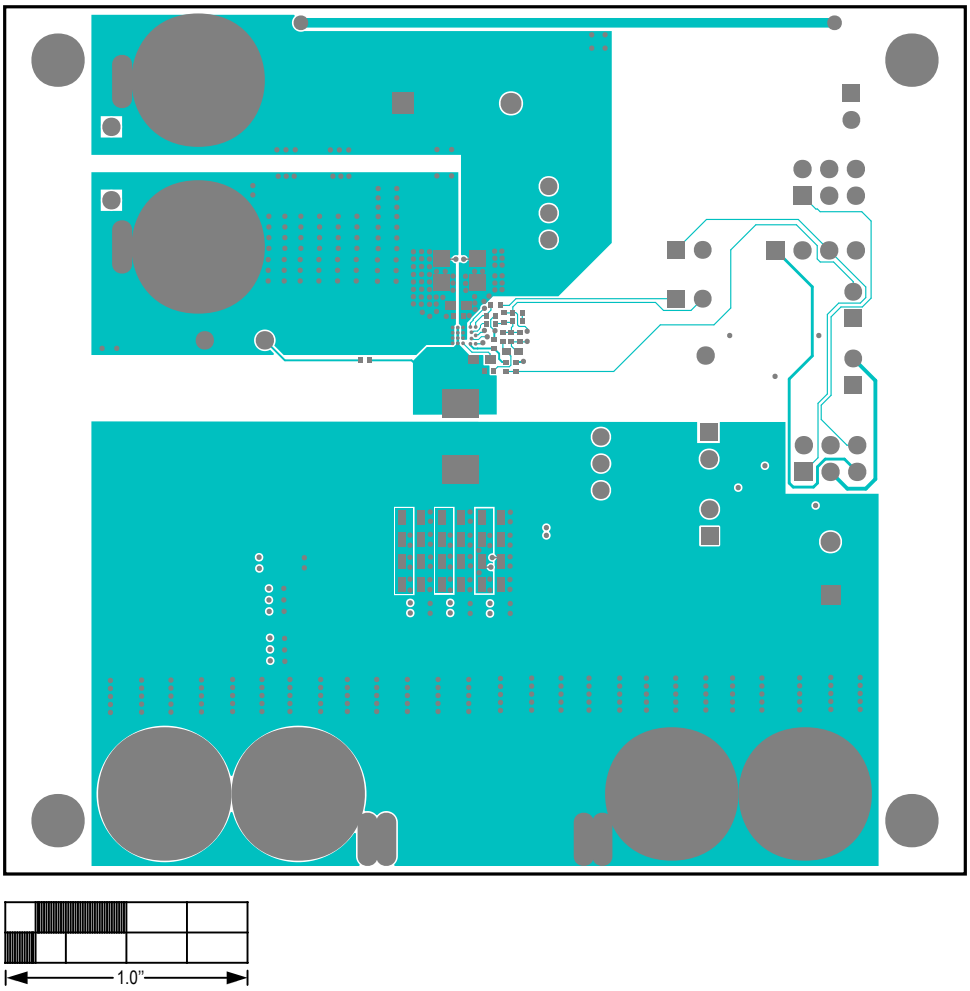


MAX38800 EV Kit PCB Layout Diagrams



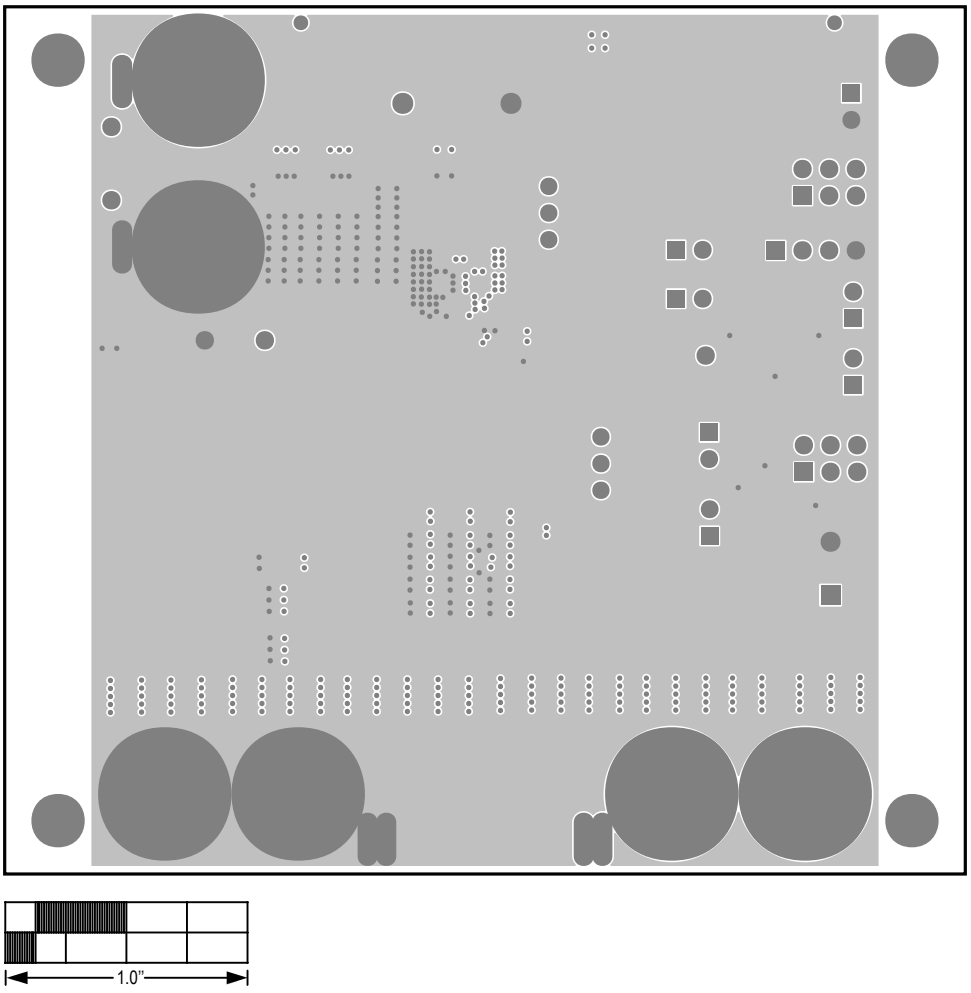
MAX38800 EV Kit—Top Silkscreen

MAX38800 EV Kit PCB Layout Diagrams (continued)



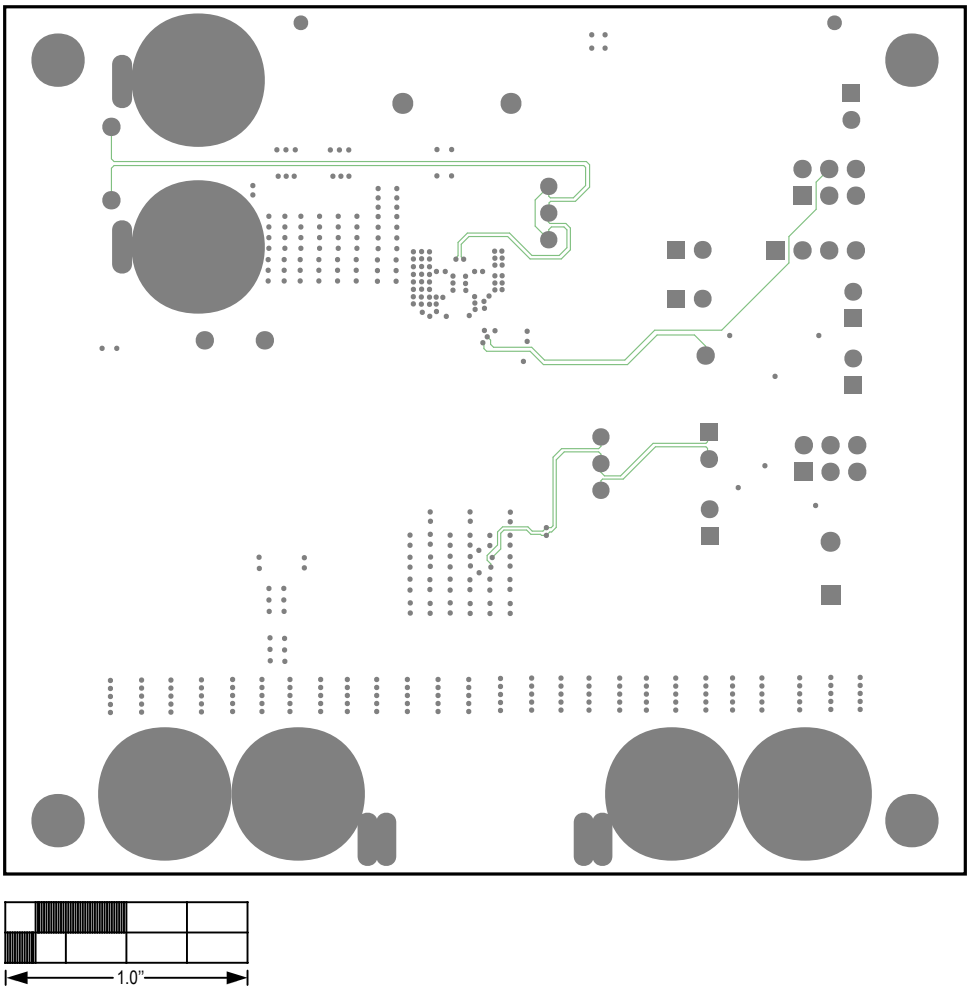
MAX38800 EV Kit—Top View

MAX38800 EV Kit PCB Layout Diagrams (continued)



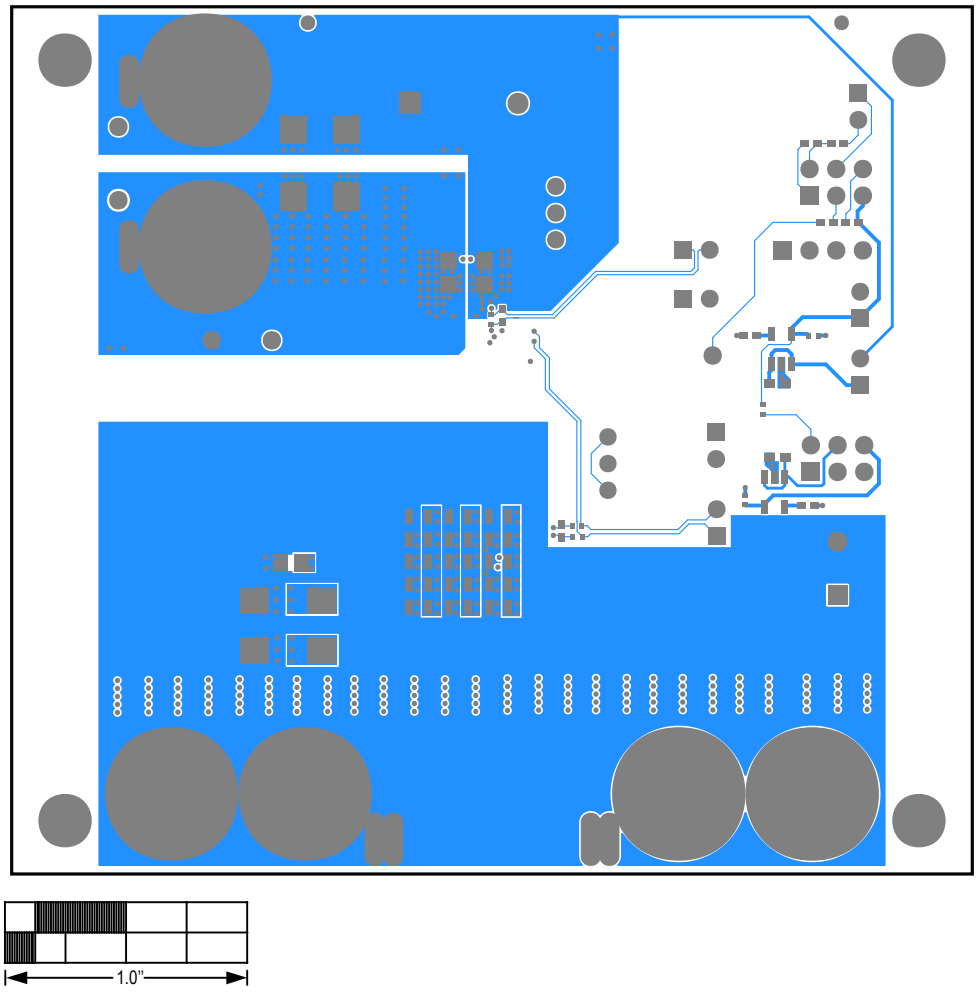
MAX38800 EV Kit—Second Layer

MAX38800 EV Kit PCB Layout Diagrams (continued)



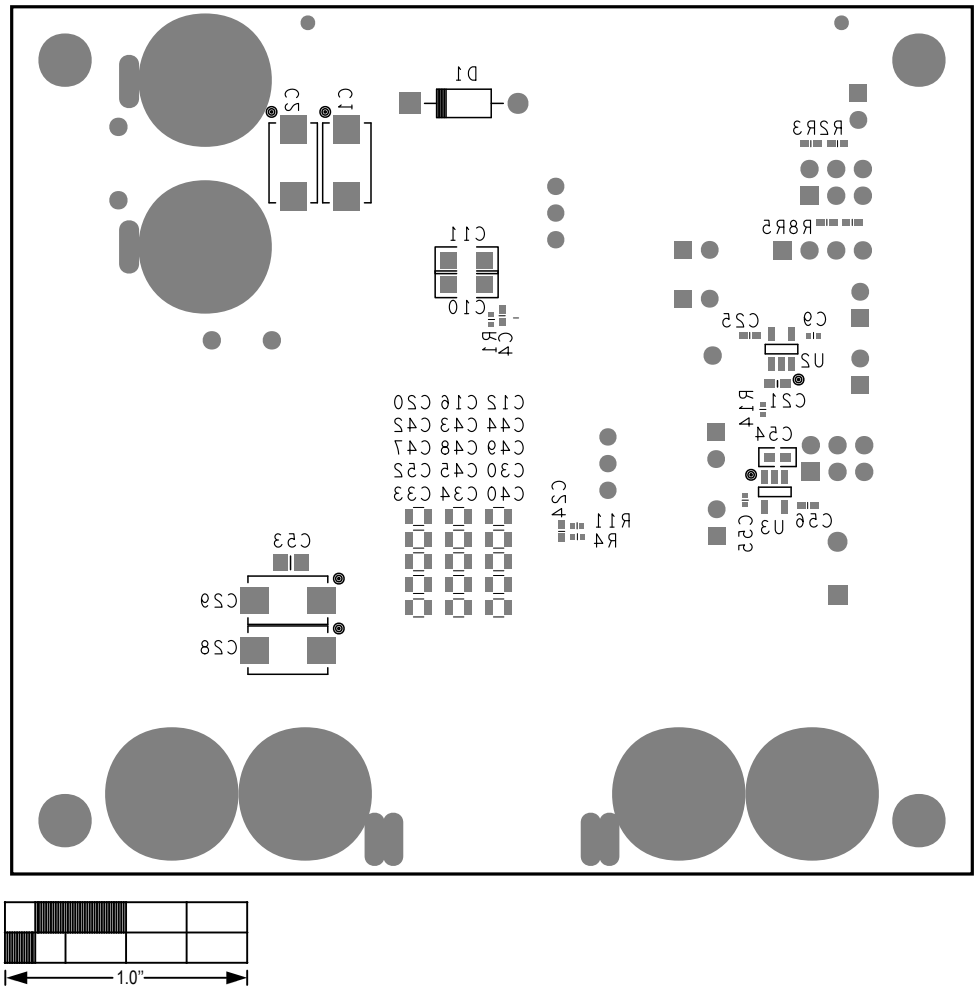
MAX38800 EV Kit—Third Layer

MAX38800 EV Kit PCB Layout Diagrams (continued)



MAX38800 EV Kit—Bottom View

MAX38800 EV Kit PCB Layout Diagrams (continued)



MAX38800 EV Kit—Bottom Silkscreen

Ordering Information

PART	TYPE
MAX38800EVKIT#	EV Kit

#Denotes an RoHS-compliant device

Revision History

REVISION NUMBER	REVISION DATE	DESCRIPTION	PAGES CHANGED
0	8/17	Initial release	—

For pricing, delivery, and ordering information, please contact Maxim Direct at 1-888-629-4642, or visit Maxim Integrated's website at www.maximintegrated.com.

Maxim Integrated cannot assume responsibility for use of any circuitry other than circuitry entirely embodied in a Maxim Integrated product. No circuit patent licenses are implied. Maxim Integrated reserves the right to change the circuitry and specifications without notice at any time.

