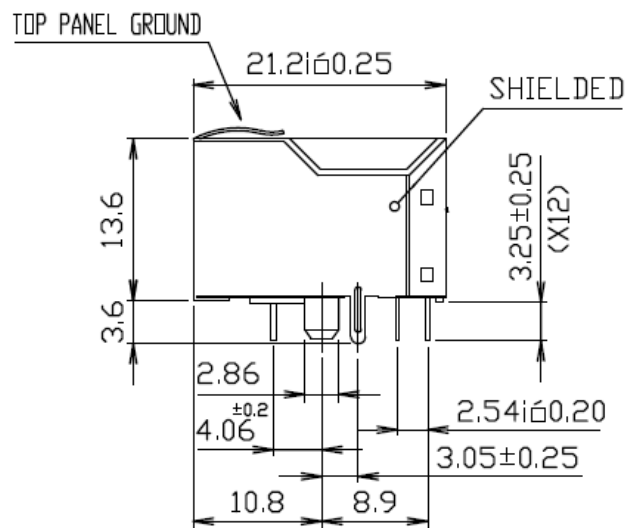
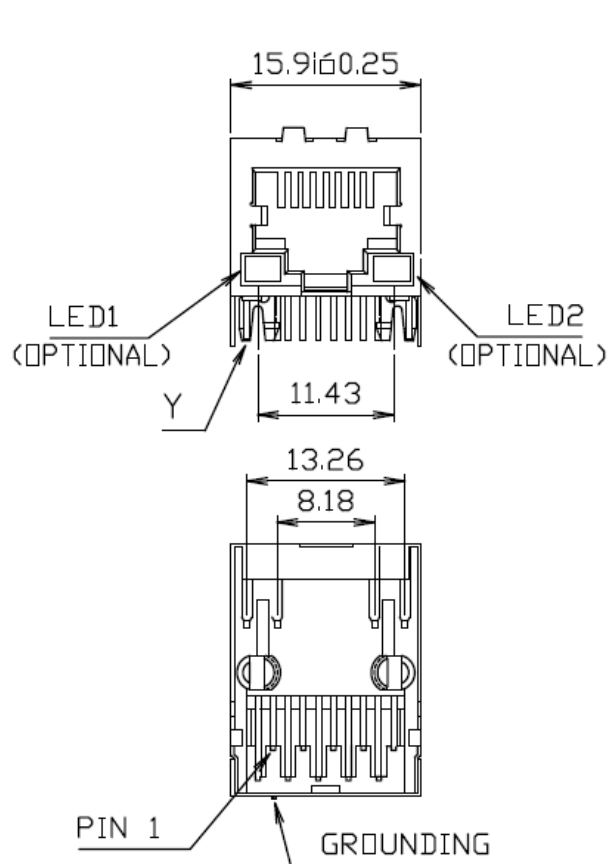
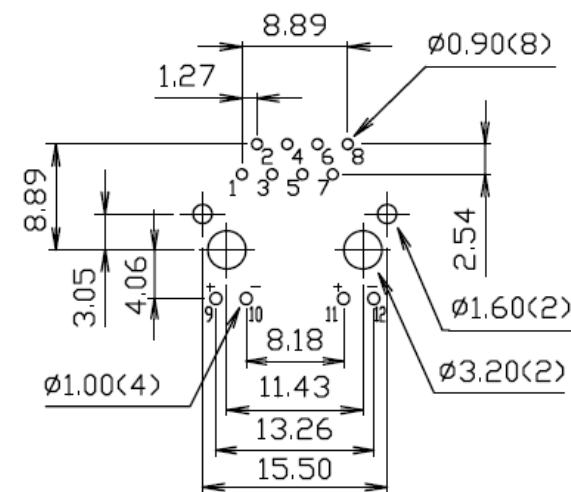


DETAIL Y

MATERIALS:

- HOUSING-PBT POLYESTER UL94V-0 MIXED GLASS FIBER, STANDARD COLOR-BLACK.
- CONTACTS-0.35mm THICK PHOS-BRONZE PLATED WITH HARD GOLD AND GOLD FLASH IN SOLDER AREA.
- SHIELD-0.25mm THICK COPPER ALLOY, PLATED WITH NICKEL.

CAVITY CONFIRMS TO FCC RULES AND REGULATIONS PART 68,SUBPART F.



PC Board Layout
Ref.



TOLERANCE (mm)		APPROVALS		DATE		TITLE RJ45 w/Transformer & LED Side Entry Shielded		AMPHENOL Amphenol European Design Center			
X.	±0.3	DRAWN Stephane		31/Oct/2007				Scale N.A.		Sheet 1 of 2	
XX	±0.2	CHECKED Rob		31/Oct/2007							
X.XX	±0.15	APPROVED Rob		31/Oct/2007							
UNLESS OTHERWISE SPECIFIED				UNIT : mm		SIZE : A4	DRAWING N° LMJ199 882X 110DTLX39J		REV. A		

ELECTRICAL CHARACTERISTICS:

1. TURNS RATIO :

(P6-P5-P4): (J6-J3)

:1CT:1CT $\pm$ 3%

(P3-P2-P1): (J2-J1)

:1CT:1CT $\pm$ 3%

2. INDUCTANCE: (P6-P4)

(P3-P1)

: 350uH MIN. @ 0.1V, 100KHz,8mA DC Bias

: 350uH MIN. @ 0.1V, 100KHz,8mA DC Bias

3. LEAKAGE INDUCTANCE: P6-P4 (WITH J6 AND J3 SHORT)

: 0.3 MAX. @ 1MHz

P3-P1 (WITH J2 AND J1 SHORT)

: 0.3 MAX. @ 1MHz

4. INTERWINDING CAPACITANCE: (P6,P5,P4)TO(J6,J3)

: 30pf MAX. @1MHz

(P3,P2,P1)TO(J2,J1)

: 30pf MAX. @1MHz

5. DC RESISTANCE: (J6-J3)=(J2-J1)

: 1.2 ohms Max.

6. CROSSTALK

: -30dB MIN AT 1MHz TO 100MHz

7. INSERTION LOSS

: -1.0dB MAX AT 1MHz TO 100MHz

8 RETURN LOSS

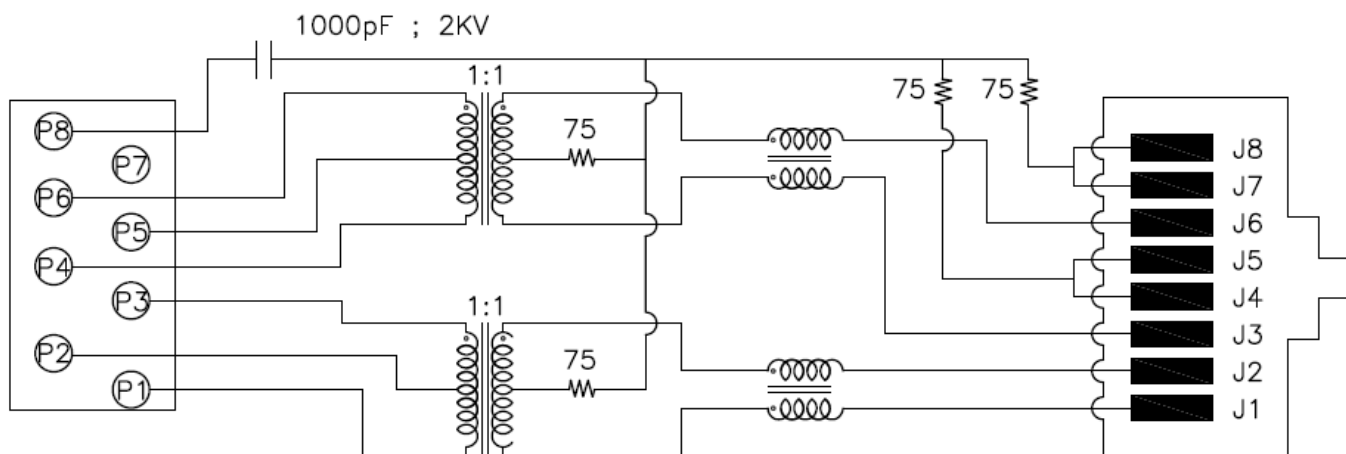
: -20dB MIN AT 1MHz TO 30MHz

-15dB MIN AT 30MHz TO 60MHz

-10dB MIN AT 60MHz TO 80MHz

NOTES

1.0 PINS WITHOUT ELECTRICAL CONNECTION ARE OMITTED



CONNECTOR SCHEMATIC



TOLERANCE (mm)	APPROVALS	DATE	TITLE		AMPHENOL	
X. ± 0.3	DRAWN Stephane	31/Oct/2007	RJ45 w/Transformer & LED Side Entry Shielded		Amphenol European Design Center	
XX ± 0.2	CHECKED Rob	31/Oct/2007				
XXX ± 0.15	APPROVED Rob	31/Oct/2007				
UNLESS OTHERWISE SPECIFIED	UNIT :	mm	SIZE : A4	DRAWING N°	LMJ199 882X 110DTLX39J	REV. A

Scale N.A. Sheet 2 of 2