



Multibhashi



Review 1



Class Objective

I will be revising all the topics and vocabulary done till today.



Concept A: Infinitiv

Infinitiv with zu

- The infinitive, which is combined with "zu", goes to the end of the clause:

- Example:

- ☐ Es wundert mich, meine Mutter hier zu sehen. (It surprises me to see my mother here.)

- ☐ Er hat vergessen, seiner Freundin etwas zum Geburtstag zu kaufen. (He forgot to buy his girlfriend something for her birthday.)



Concept A: Infinitiv

- If the verb in question has a separable prefix, the zu goes between the prefix and the stem (e.g. anzufangen [to begin], zuzumachen [to close]).
- Traditionally, an infinitive clause that contains only the verb and possibly an adverb is not set off by a comma, while those with other elements are :
 - o Example:
 - ☐ § Das ist schwer zu machen. (That's hard to do)
 - ☐ § Er hat vor, eine Torte zu backen. (He intends to bake a cake.)



Concept A: Infinitiv

Infinitiv with um...zu

- German uses um ... zu in order to express intention. This construction can usually be translated by "in order to":
 - o Example:
 - ❑ Sie kommen nach Deutschland. Sie wollen Musik studieren.
 - ❑ Sie kommen nach Deutschland, um Musik zu studieren.
(They're coming to Germany in order to study music.)



Concept A: Infinitiv

Infinitiv with lassen

- The equivalents of "lassen" in English:
- The verbs "to leave" and "to let" correspond to "lassen," which explains the existence of "leave him be" beside the more standard "let him be." Note that both English verbs combine with an infinitive without "to."
- "Lassen" in German has several distinct functions.

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INSTITUTE OF TECHNOLOGY
DEPARTMENT OF ELECTRICAL ENGINEERING
EE-561: ADVANCED TOPICS IN SIGNAL PROCESSING
LECTURE 1: INTRODUCTION TO THE COURSE

1.1 COURSE OBJECTIVES

1.2 COURSE STRUCTURE

1.3 COURSE MATERIALS

1.4 COURSE SCHEDULE

1.5 COURSE FACULTY

1.6 COURSE CONTACTS

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EECS 441: ADVANCED TOPICS IN SIGNAL PROCESSING
LECTURE 1: INTRODUCTION TO ADVANCED TOPICS

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EE-440: POWER SYSTEMS
LECTURE 1: INTRODUCTION

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EECS 441: DIGITAL SIGNAL PROCESSING
LECTURE 10: DISCRETE-TIME SYSTEMS

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1.2. DISCRETE-TIME SYSTEMS

1.3. SYSTEM REPRESENTATION

1.4. SYSTEM ANALYSIS

1.5. SYSTEM DESIGN

1.6. SUMMARY