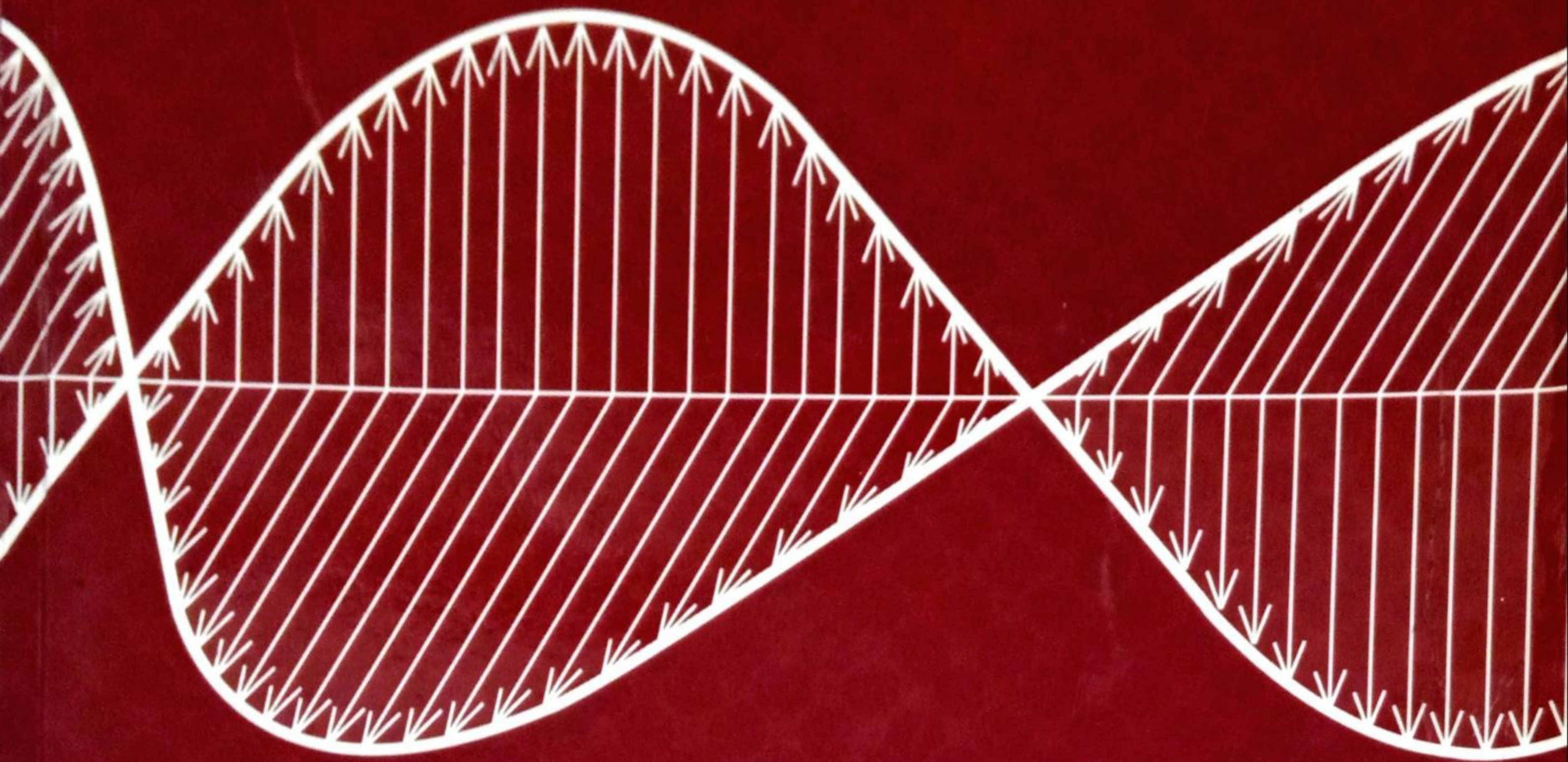


Second Edition



**MILLMAN'S**  
**INTEGRATED**  
**ELECTRONICS**

*Analog and Digital Circuits and Systems*

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