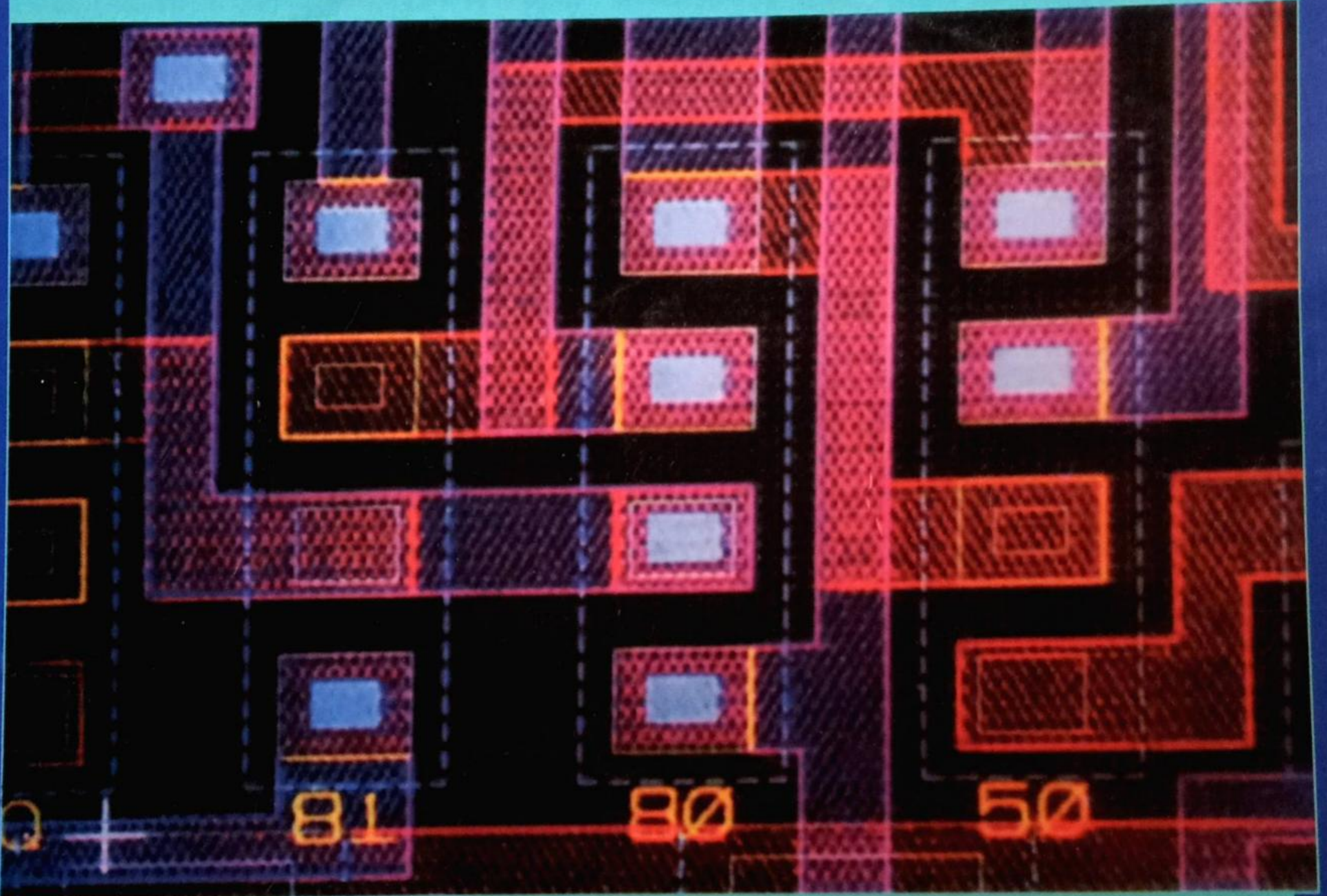


Second Edition

MICROELECTRONICS

Jacob Millman
Arvin Grabel



**Mc
Graw
Hill**
Education



INDIAN EDITION

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